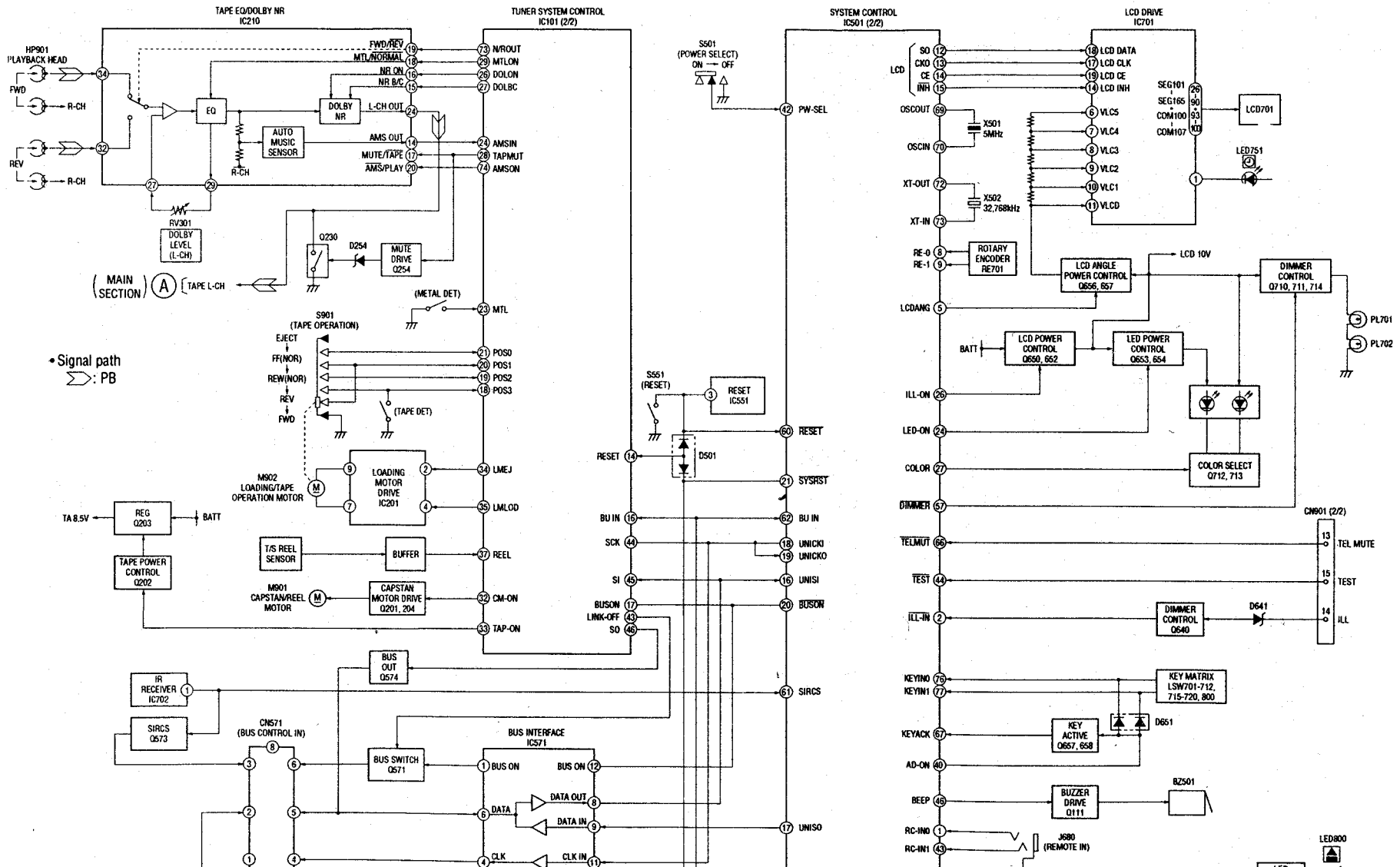


# SONY XR-C750RDS/C850RDS XR-C850RW

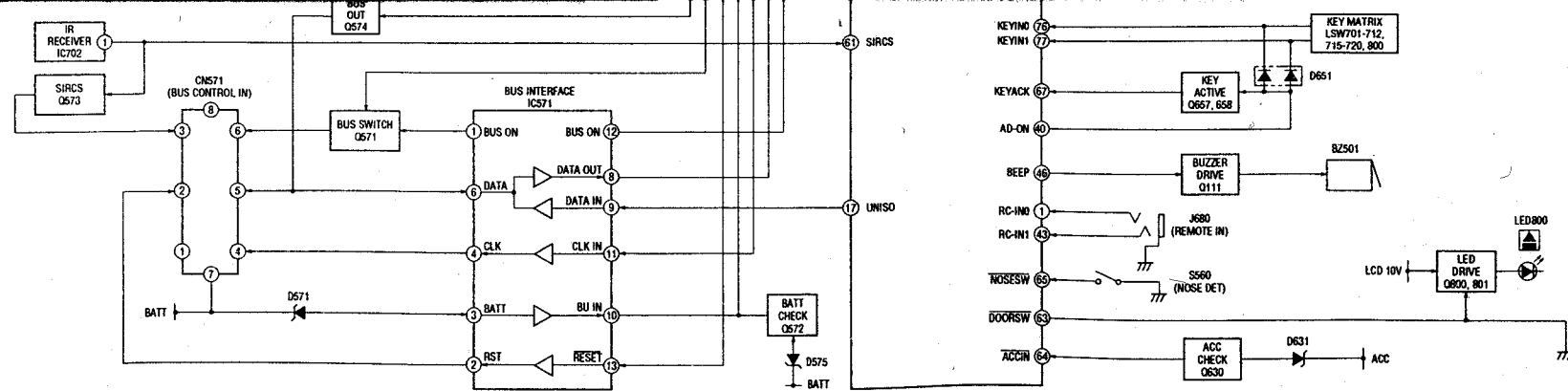
This is an XR-C850RDS that has a grain finished front panel.  
The differences from the XR-C850RDS are only described herein.  
For other information, see the service manual for XR-C750RDS/  
C850RDS.

Model Name Using Similar Mechanism	XR-C700RDS
Tape Transport Mechanism Type	MG-52B-135

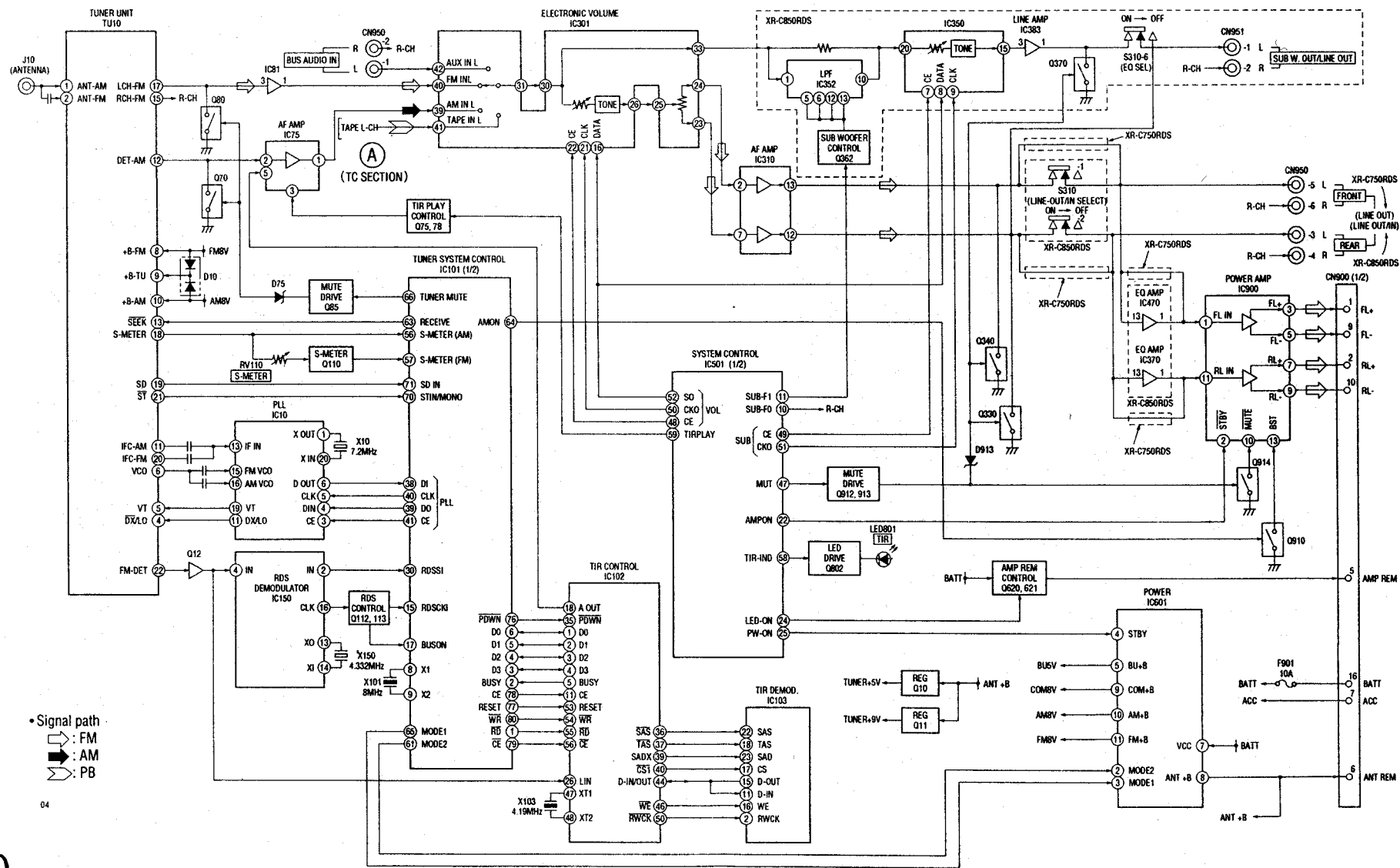
## BLOCK DIAGRAM — TC SECTION —



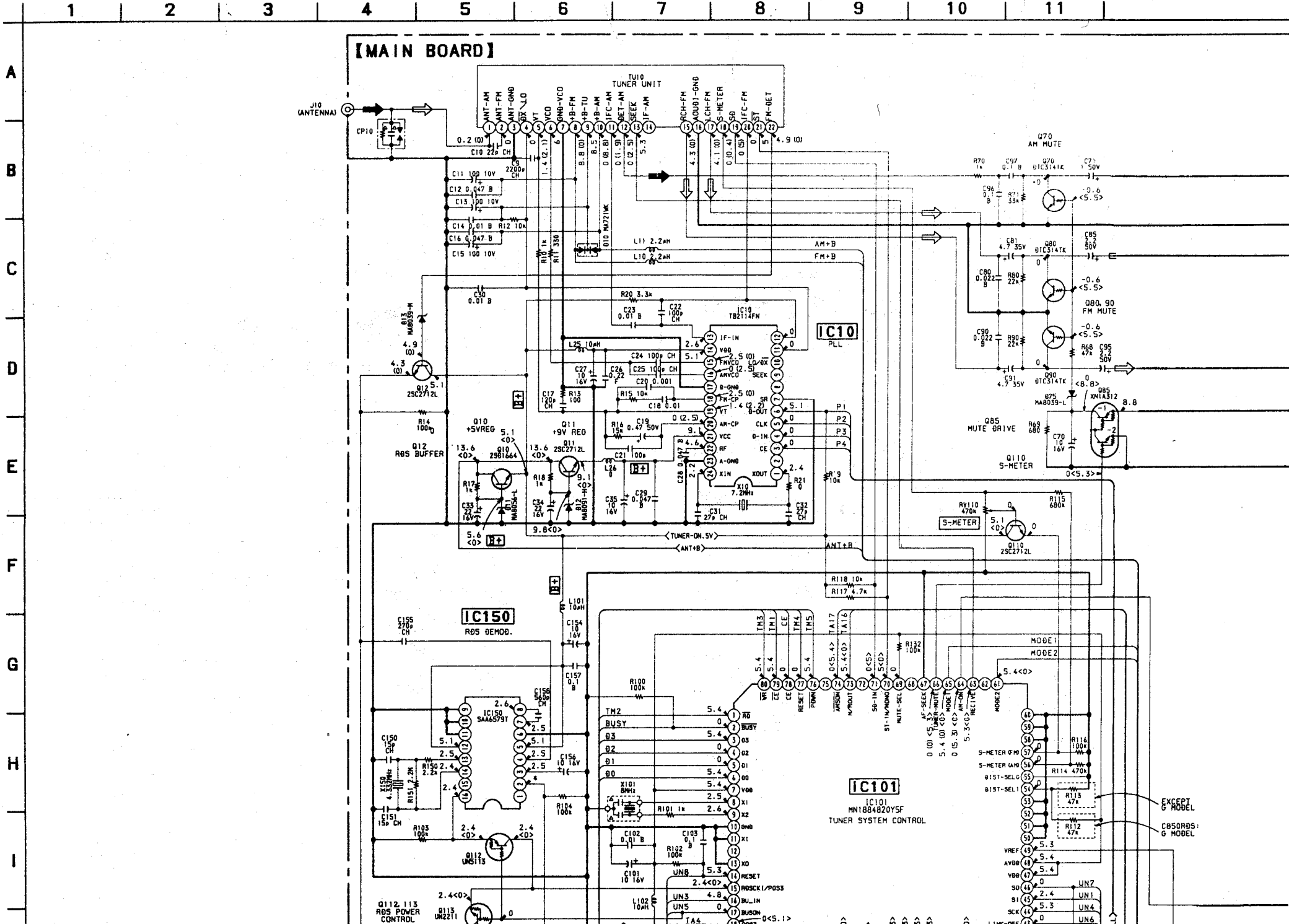
Page	XR-C850RDS			XR-C850RW			
	Ref. No.	Part No.	Description	Remark	Part No.	Description	Remark
60	56	3-010-983-01	BUTTON (OPEN)		3-010-983-12	BUTTON (OPEN)	
	58	3-010-982-01	BUTTON (SOUND)		3-010-982-11	BUTTON (SOUND)	
	59	3-010-981-01	BUTTON (OFF)		3-010-981-11	BUTTON (OFF)	
	60	3-010-980-01	BUTTON (10 KEY)		3-010-980-12	BUTTON (10 KEY)	
	68	3-010-977-01	PANEL, FRONT BACK		3-010-977-11	PANEL, FRONT BACK	
69	X-3373-706-1	PANEL SUB ASSY. FRONT (C850RDS)			X-3374-172-1	PANEL SUB ASSY. FRONT	
73	1-473-067-31	REMOTE COMMANDER (RM-X2S)			1-473-067-51	REMOTE COMMANDER (RM-X2S)	
	206	3-012-213-01	COLLAR		3-012-213-11	COLLAR	



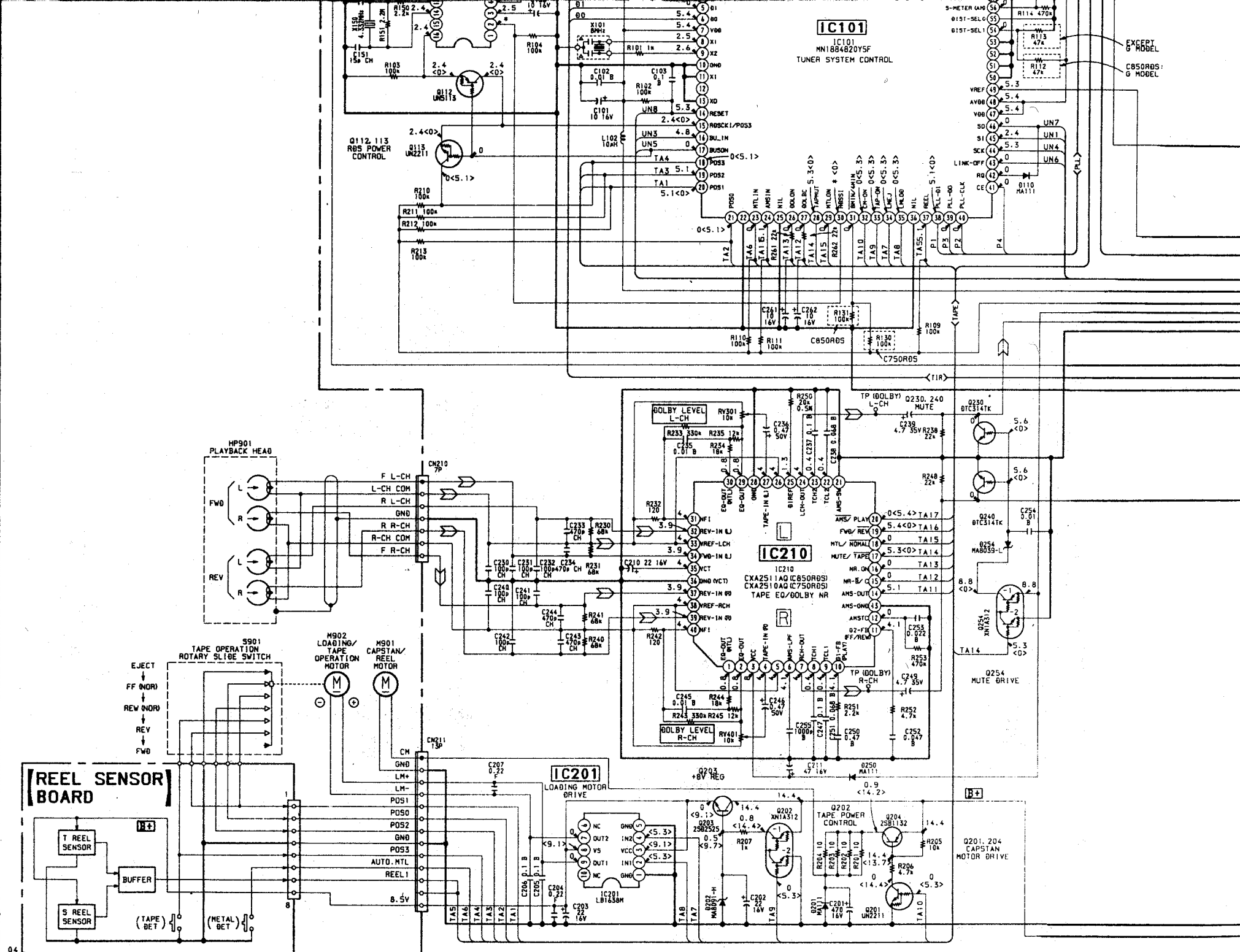
**BLOCK DIAGRAM — MAIN SECTION —**

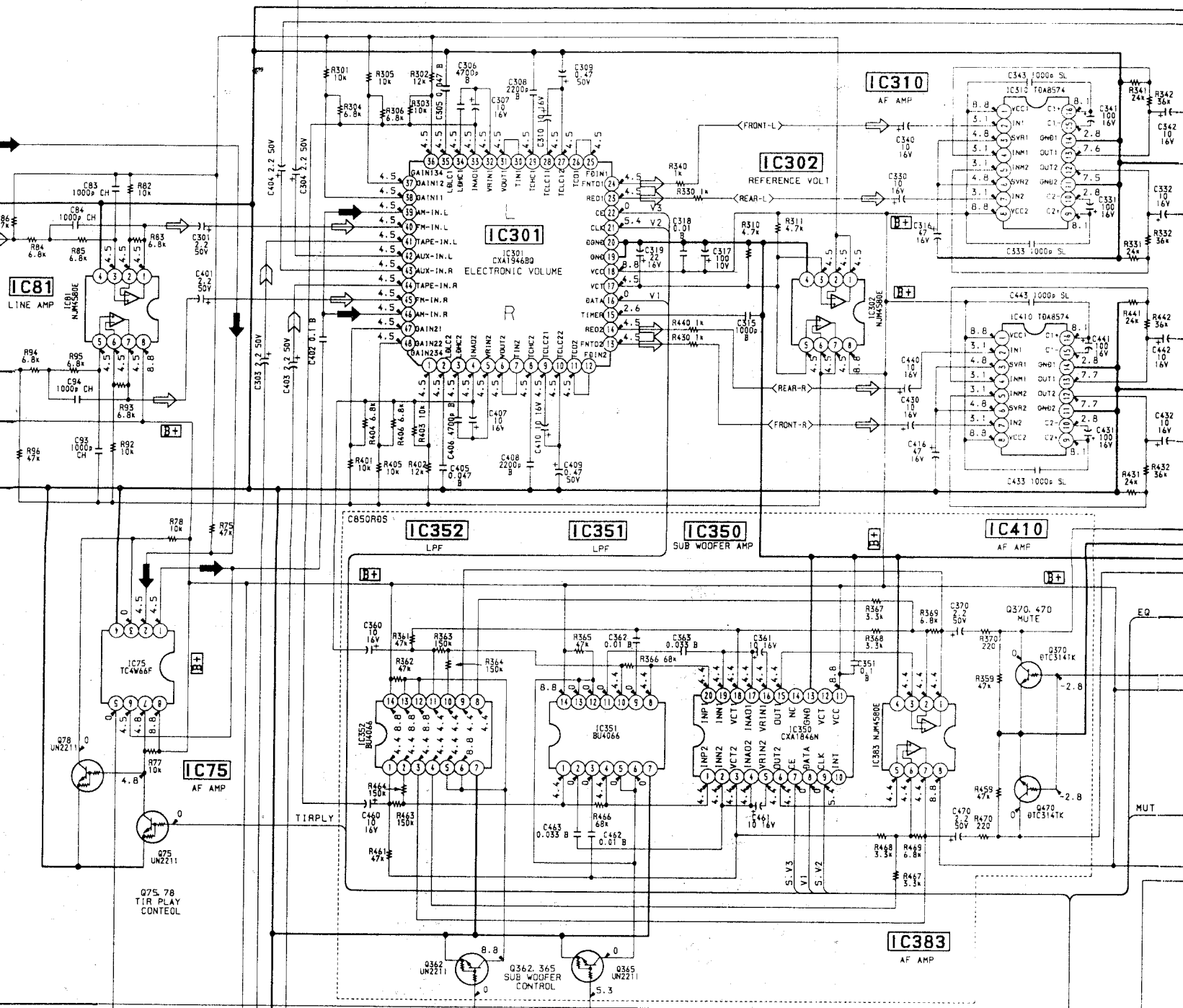


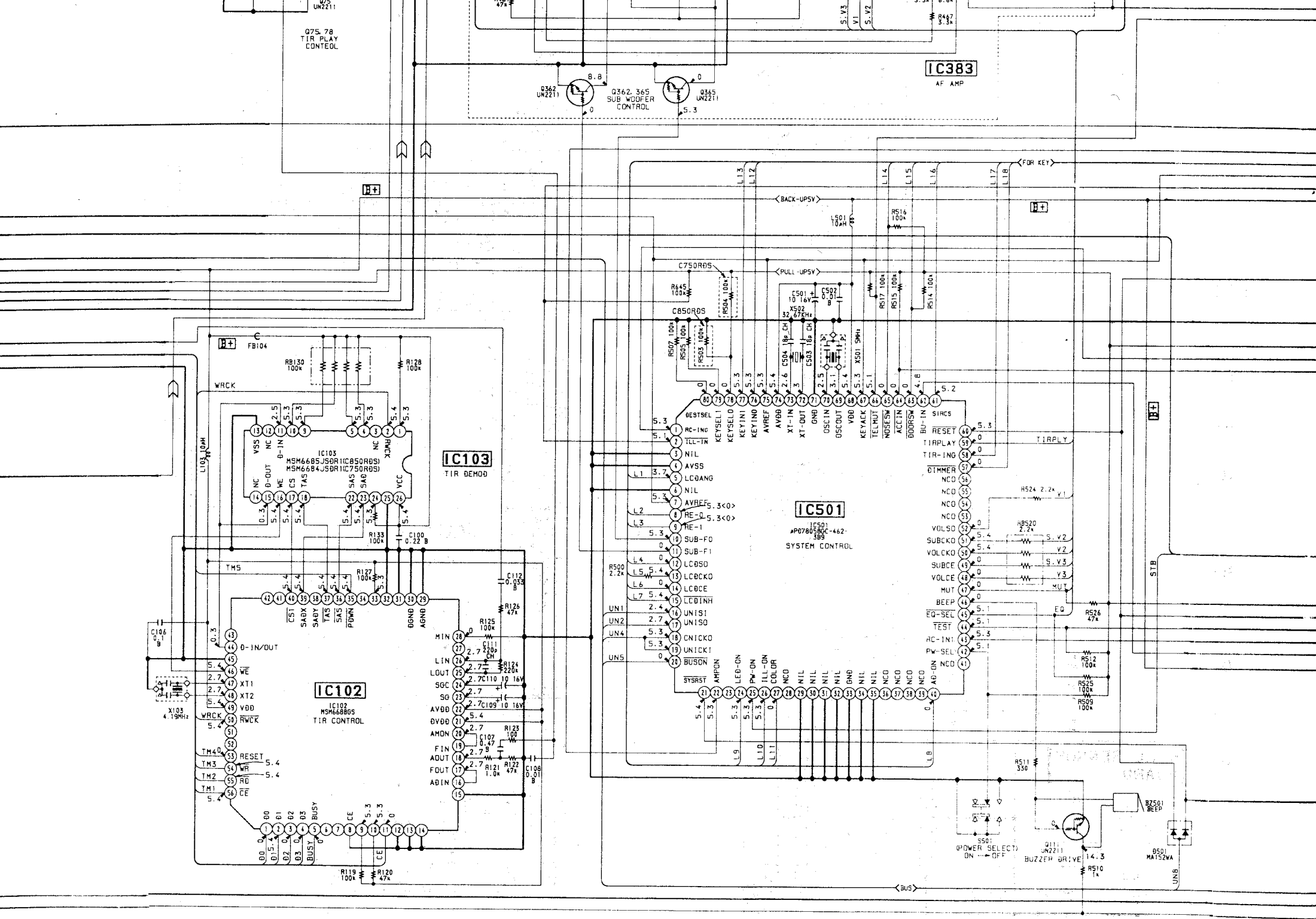
6-5. SCHEMATIC DIAGRAM — MAIN SECTION — • Refer to page 53 for IC Block Diagrams.



H  
I  
J  
K  
L  
M  
N  
O  
P  
Q







Q75, 78  
TIR PLAY  
CONTROL

Q362 UN2211  
Q365 UN2211  
SUB WOOFER  
CONTROL

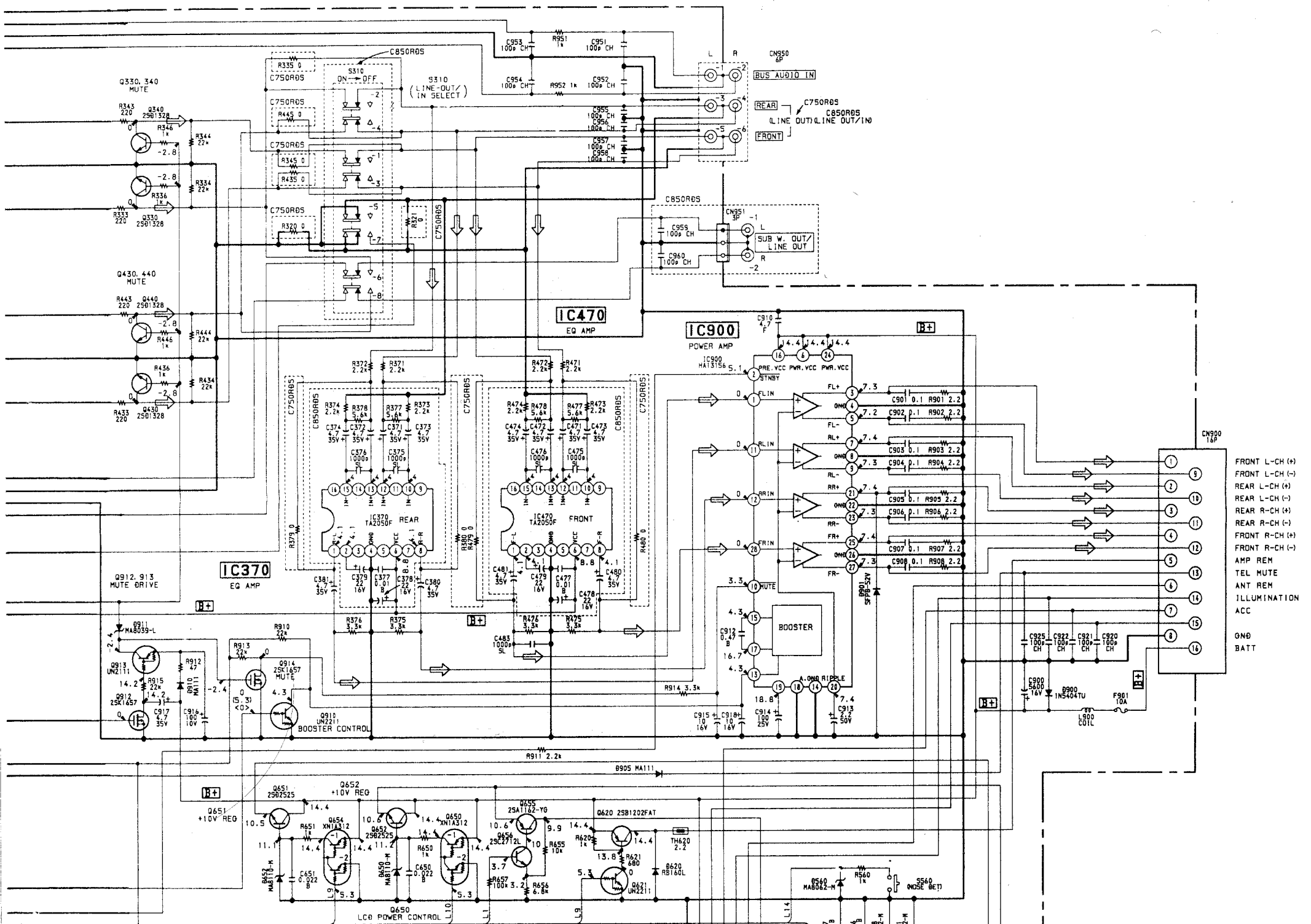
IC383  
AF AMP

FB104

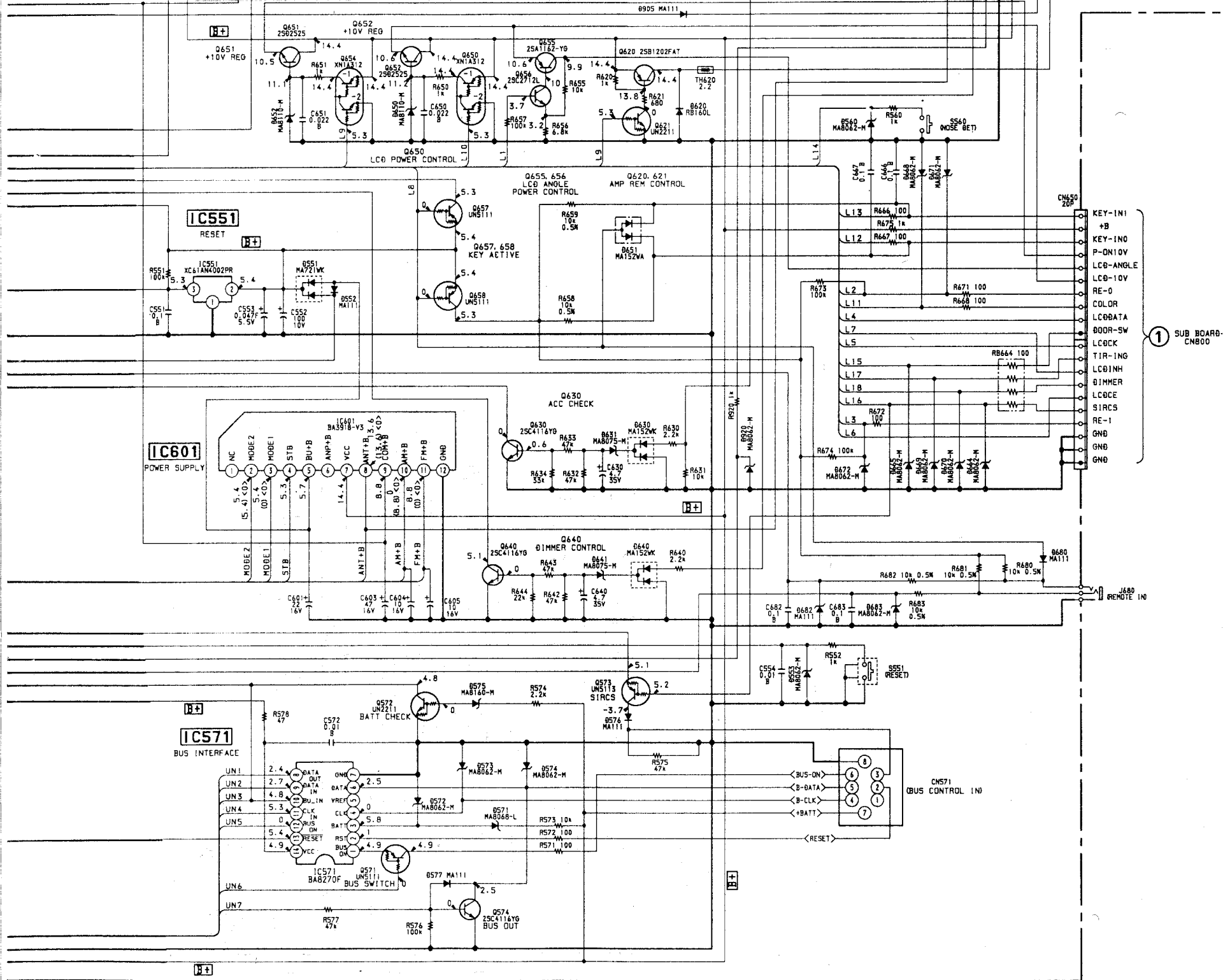
IC103  
TIR DEMO

IC501  
SYSTEM CONTROL

IC102  
TIR CONTROL

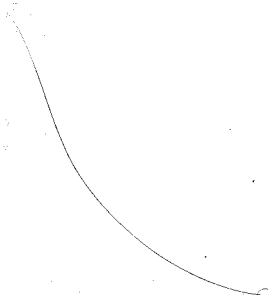


- 1 FRONT L-CH (+)
- 2 FRONT L-CH (-)
- 3 REAR L-CH (+)
- 4 REAR L-CH (-)
- 5 REAR R-CH (+)
- 6 REAR R-CH (-)
- 7 FRONT R-CH (+)
- 8 FRONT R-CH (-)
- 9 AMP REM
- 10 TEL MUTE
- 11 ANT REM
- 12 ILLUMINATION
- 13 ACC
- 14 GND
- 15 BATT





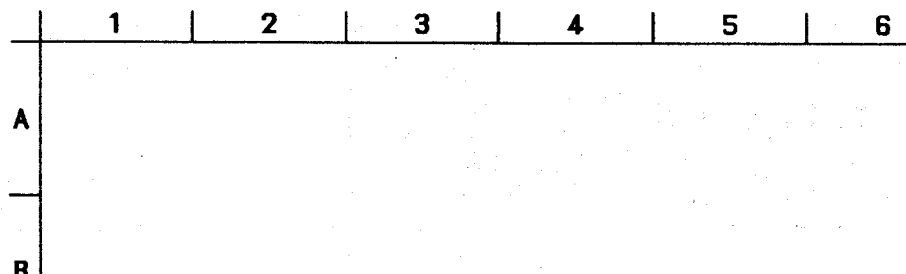
- Signal path.
- ◁ : FM
- ▣ : MW
- ▷ : PB



**Note :**

- All capacitors are in  $\mu\text{F}$  unless otherwise noted. pF :  $\mu\text{pF}$  50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in  $\Omega$  and 1/4 W or less unless otherwise specified.
- $\Delta$  : internal component.
- $\square$  : B+ Line.
- $\square$  : adjustment for repair.
- Power voltage is dc 14.4 V and fed with regulated dc power supply from ACC and BATT terminals.
- Voltage is dc with respect to ground under no-signal (detuned) conditions.
- no mark : FM
- ( ) : MW
- < > : PB
- \* : impossible measurement point.
- Voltage are taken with a VOM (Input Impedance 10M $\Omega$ ). Voltage variations may be noted due to normal production tolerance.
- Signal path.
- ◁ : FM
- ▣ : MW
- ▷ : PB
- Abbreviation
- G : German model

**6-7. SCHEMATIC DIAGRAM — PANEL SECTION —**

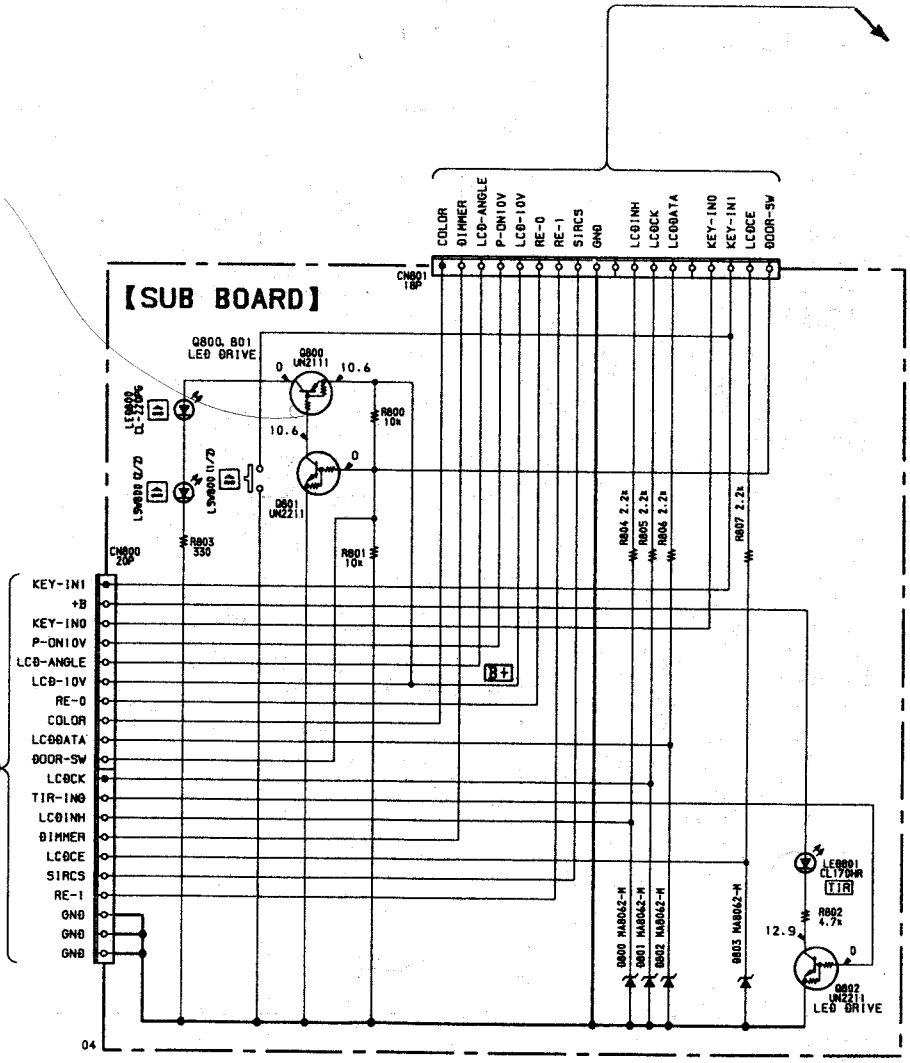


1 2 3 4 5 6

A  
B  
C  
D  
E  
F  
G  
H  
I  
J

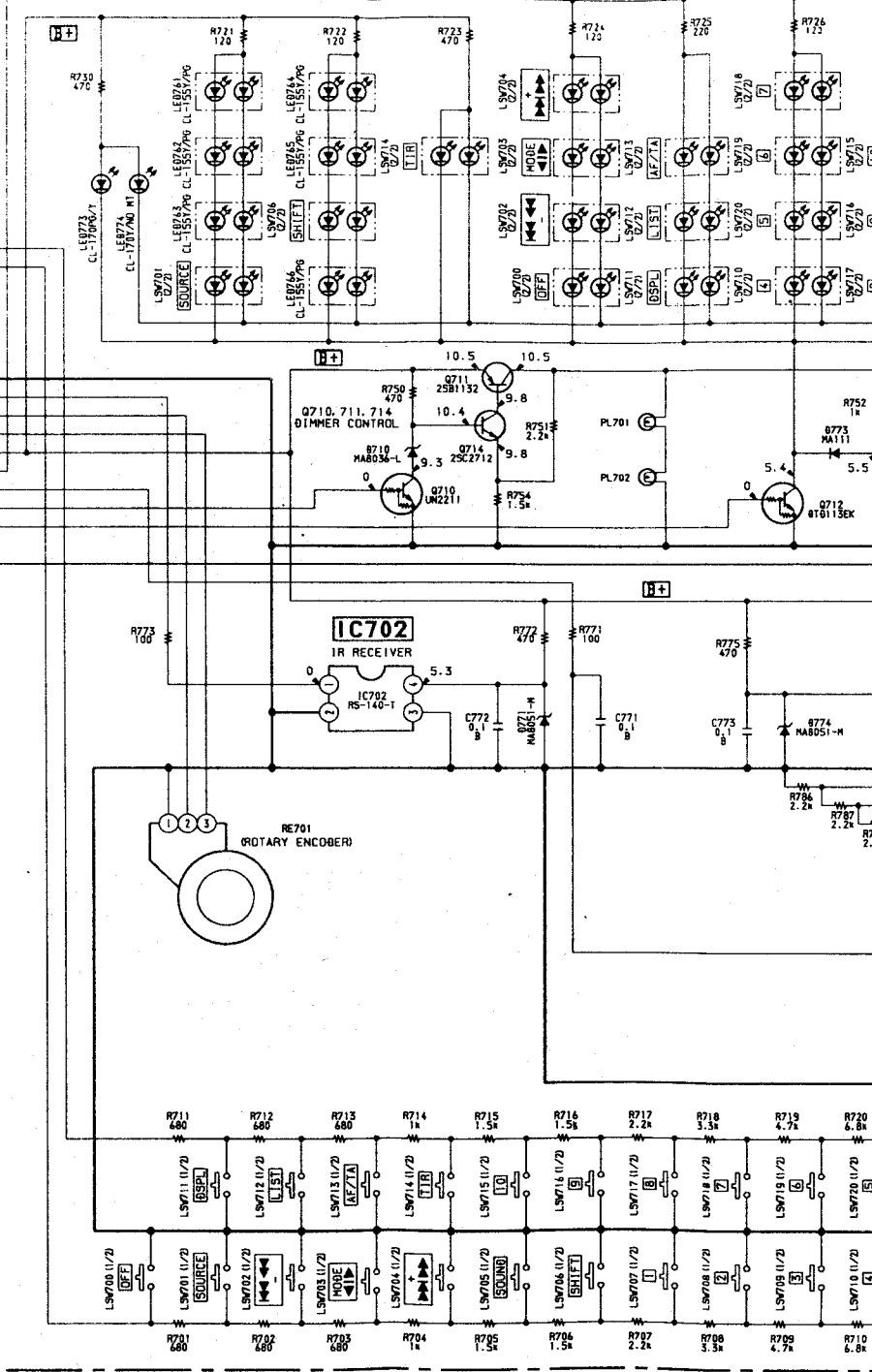
MAIN BOARD CN650 ①

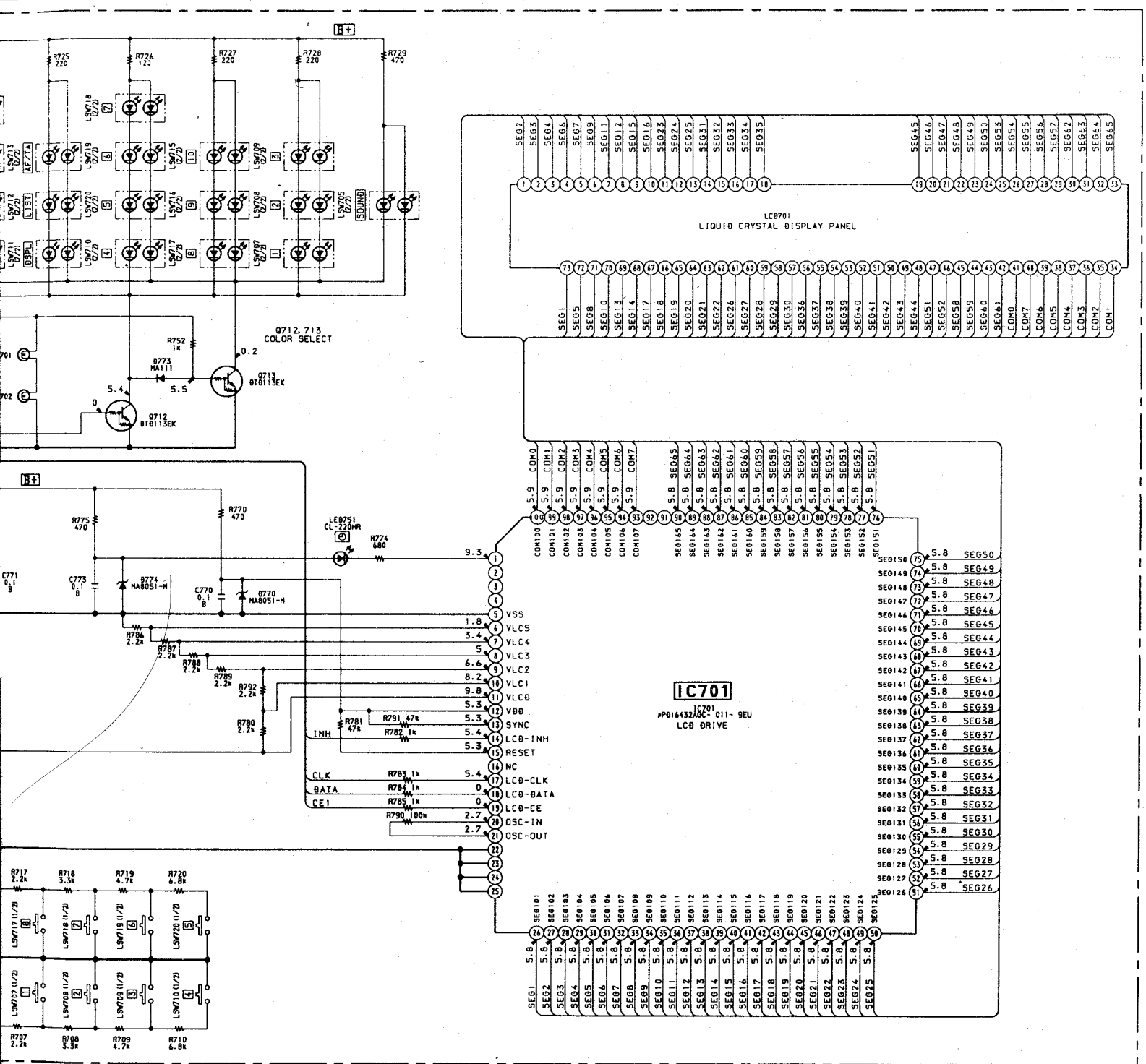
**[SUB BOARD]**



### [KEY BOARD]

- DOOR-SW
- LC0-CE1
- KEY-IN1
- KEY-IN0
- LC0-DATA
- LC0-CLK
- LC0-INH
- GN0
- SIRCS
- RE-1
- RE-0
- LC0+B
- P-ON+B
- ANGLE
- BIMMER
- COLOR





- Note :**
- All capacitors are in  $\mu\text{F}$  unless otherwise noted.  $\text{pF}$  :  $\mu\text{F}$  50 WV or less are not indicated except for electrolytics and tantalums.
  - All resistors are in  $\Omega$  and 1/4 W or less unless otherwise specified.
  - **[B+]** : B+ Line.
  - Power voltage is dc 14.4 V and fed with regulated dc power supply from ACC and BATT terminals.
  - Voltage is dc with respect to ground under no-signal (detuned) conditions.
  - Voltage are taken with a VOM (Input Impedance 10M $\Omega$ ). Voltage variations may be noted due to normal production tolerance.