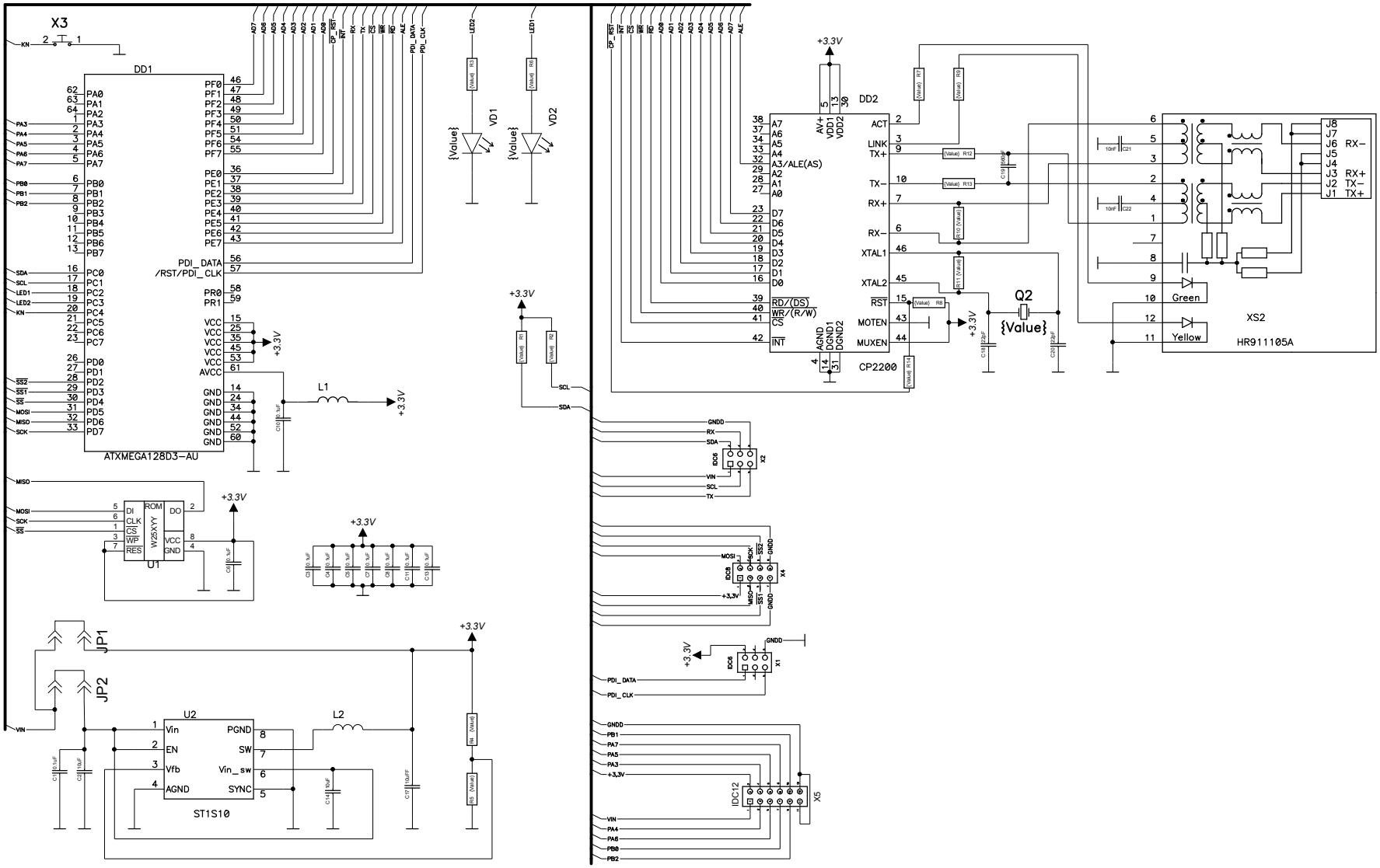
X3
DD1
ATXMEGA128D3-AU
PDI_DATA
/RST/PDI_CLK
VD1
VD2
{Value}
L1
+3.3V
U1
W25XYY
ROM
DI
CLK
CS
WP
RES
DO
VCC
GND
JP1
JP2
U2
ST1S10
Vin
EN
Vfb
AGND
PGND
SW
Vin_sw
SYNC
L2
DD2
CP2200
ACT
LINK
TX+
TX-
RX+
RX-
XTAL1
XTAL2
RST
MOTEN
MUXEN
RD/(DS)
WR/(R/W)
CS
INT
AGND
DGND1
DGND2
Q2
XS2
HR911105A
Green
Yellow
J8
J7
J6
J5
J4
J3
J2
J1
X2
X4
X1
X5
IDC6
IDC8
IDC12