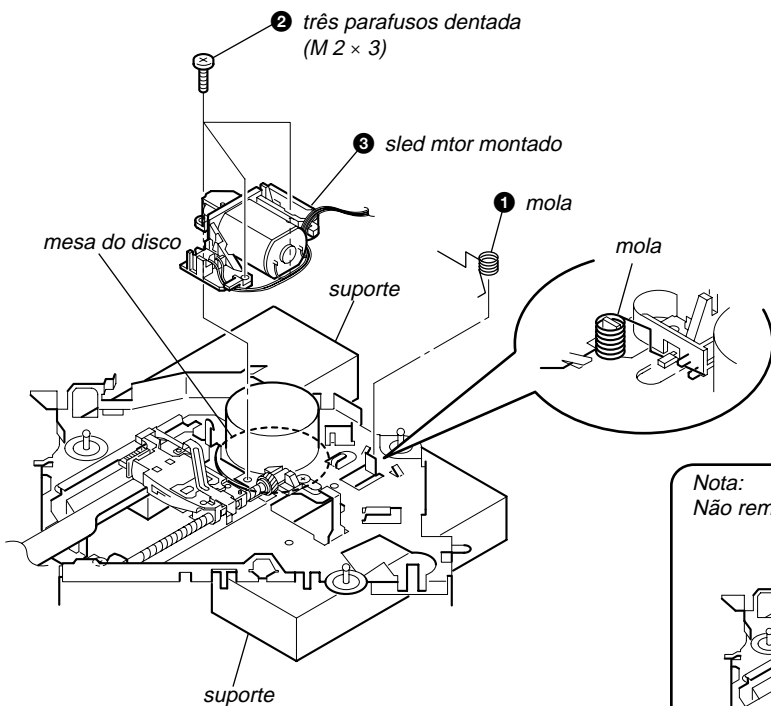
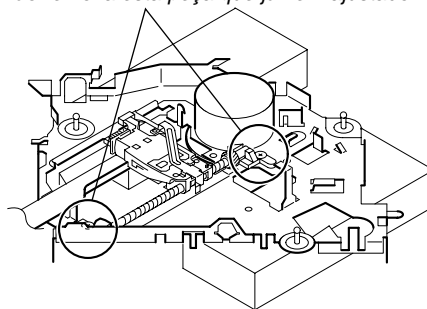


### 3-9. SLED MOTOR MONTADO

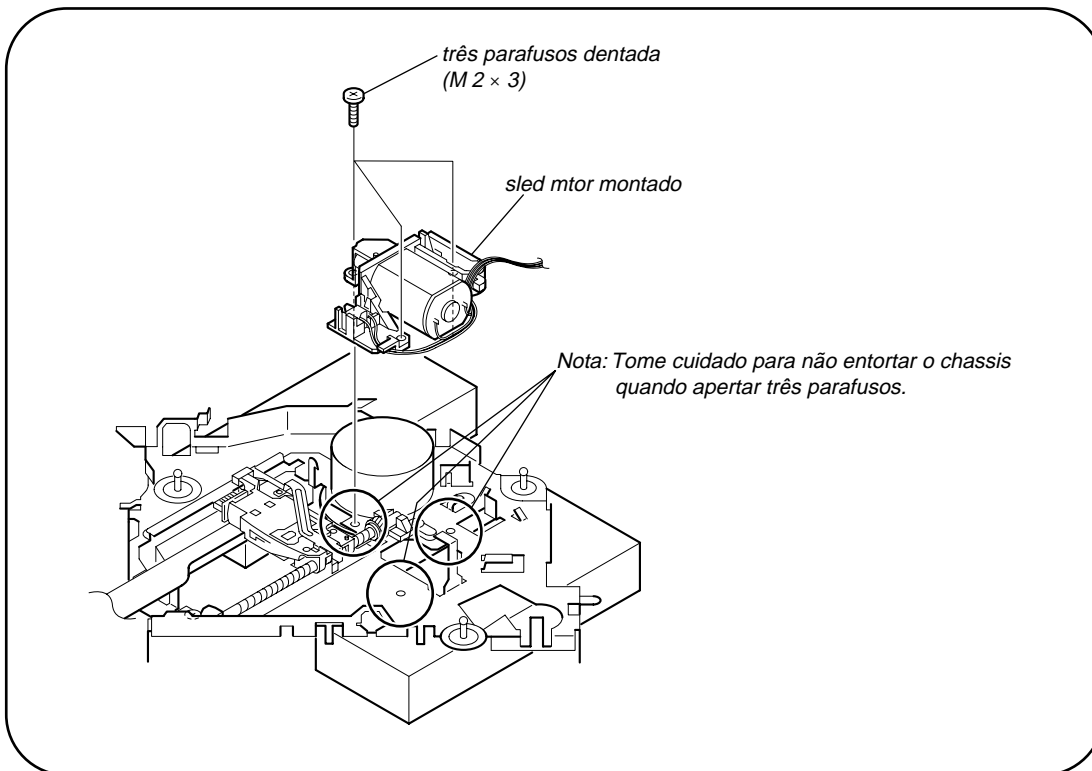


Nota: Arrume suporte para que motor do disco não tocar

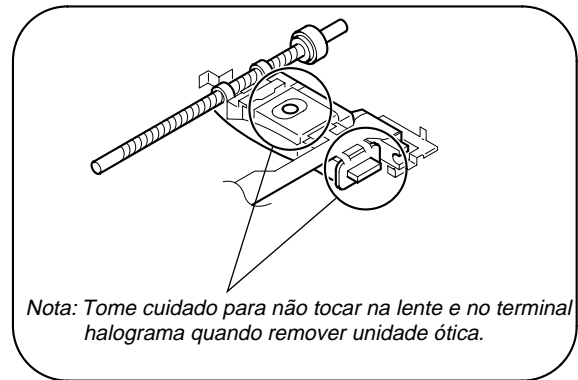
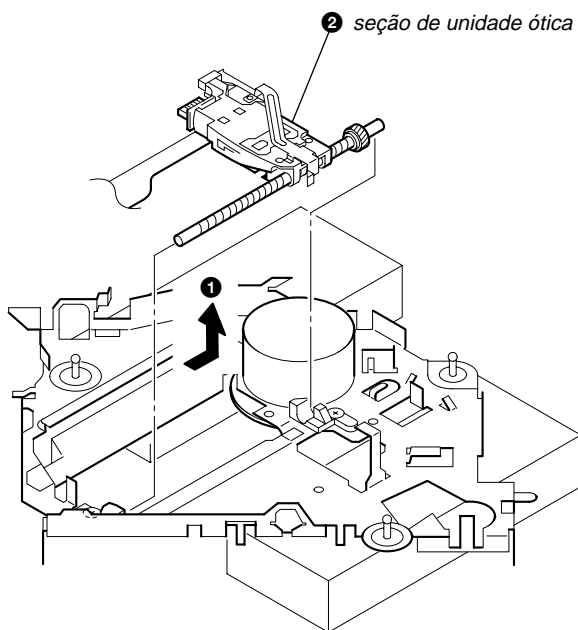
Nota:  
Não remova esta peça que já vem ajustado.



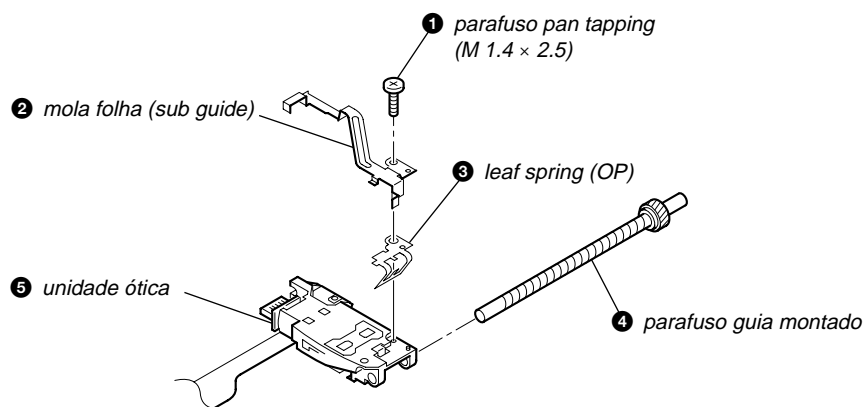
Nota para Montagem



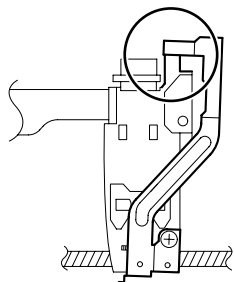
### 3-10. SEÇÃO DE UNIDADE ÓTICA



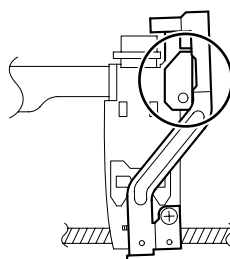
### 3-11. OPTICAL PICK-UP



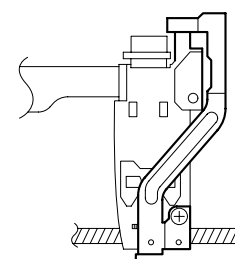
Notas para montagem



*Evite contato com lateral da mola folha e lateral da base de Unidade Ótica.*



*Evite contato com lateral da mola folha e lateral da base de Unidade Ótica.*



*Este é o espaço de lateral da mola folha entre a base da Unidade ótica.*

# SEÇÃO 4 FUNÇÃO DE DIAGNÓSTICO

## Descrição do Função de Diagnóstico:

### 1. Colocando no modo visualização do Diag

Com aparelho desligado, pressione as teclas **[4/SHUF]**, **[5]** e **[4/SHUF]** no aparelho ou no controle remoto (mais que 2 segundos) para ativar.

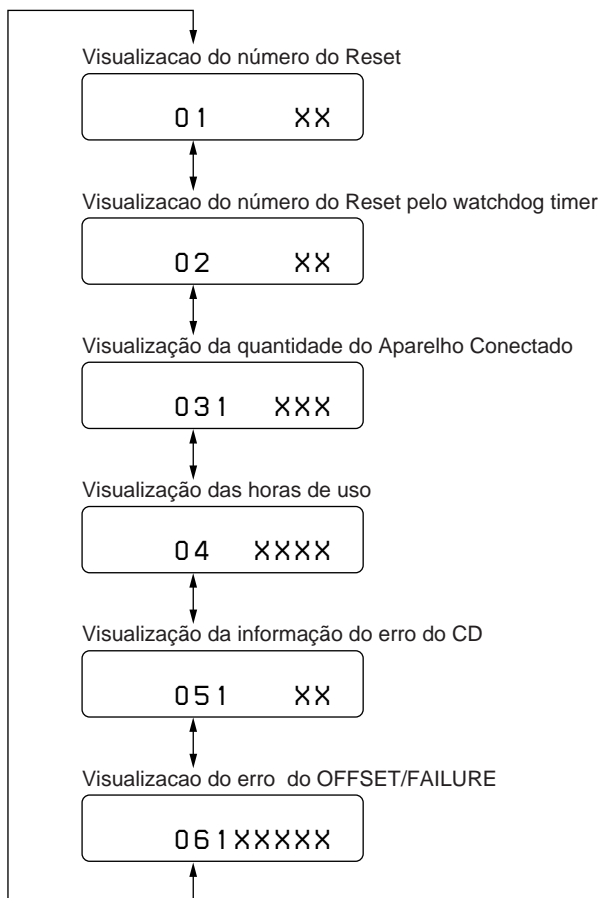
### 2. Cancelando o modo de visualização do Diag

Durante no modo de função do Diag, pressione a tecla **[OFF]**.

### 3. Visualização inicial em modo do Diag.

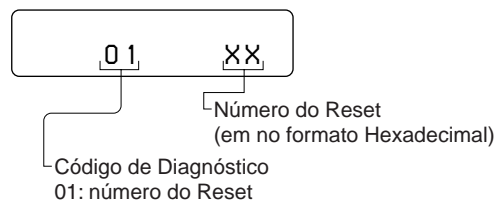
Apenas quando o modo Diag e ativado, "reset count" é mostrado. Esse modo e visualizado cada vez que pressionar a tecla

**[▶▶▶/SEEK +]** ou **[◀◀◀/SEEK -]** ..

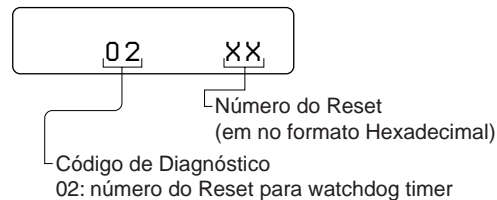


## 4. Modo de visualização de cada itens.

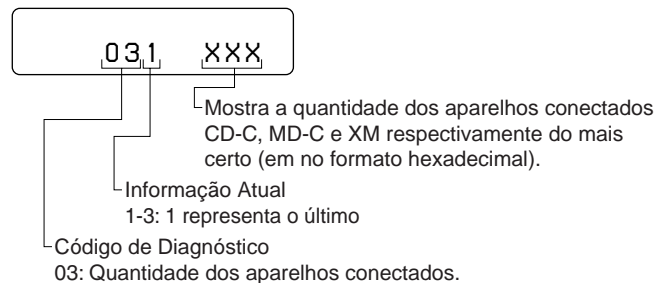
### 4-1. Modo de visualização do número do Reset



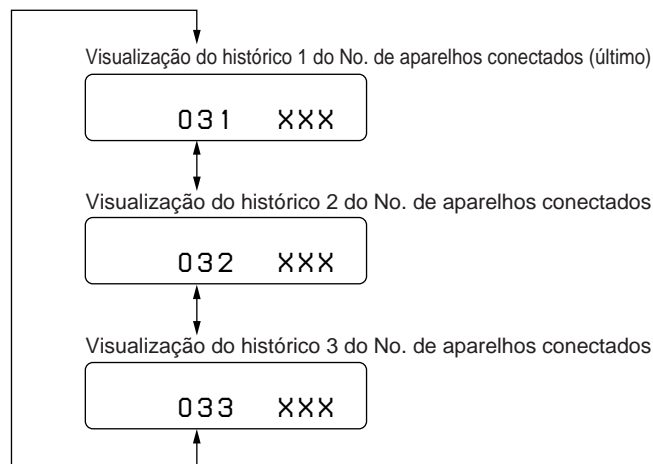
### 4-2. Modo de visualização do número do Reset pelo watchdog timer



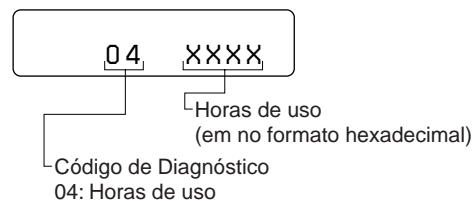
### 4-3. Modo de Visualização da quantidade do Aparelho Conectado

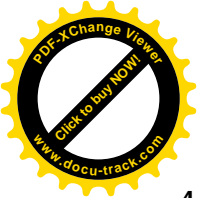


Esse modo e visualizado cada vez que pressionar a tecla **[2/ALBM+]** ou **[1/ALBM-]** durante no modo de visualização da quantidade do aparelho conectado.



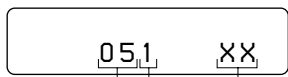
### 4-4. Visualização do modo das horas de uso.





### 4-5. Modo de visualização da informação do erro do CD

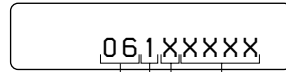
#### 4-5-1. Descrição do Erro



Descrição do erro (em no formato Hexadecimal)  
 Informação Atual 1-3: 1 representa o último  
 Código de Diagnóstico 05: Informação do erro do CD

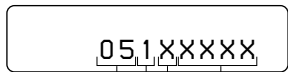
Indicação	Descrição
1X	SERVO ERROR
3X	LOADING ERROR
4X	TRACK JUMP
5X	TEXT ERROR
FX	MECHA ERROR

### 4-6. Modo de visualização do erro OFFSET/FAILURE



Horas de uso  
 Descrição do erro (0: OFFSET, 1: FAILURE)  
 Informação Atual 1-3: 1 representa o último  
 Código de Diagnóstico 06: OFFSET/FAILURE

#### 4-5-2. Tipo de Disco e Horas de uso

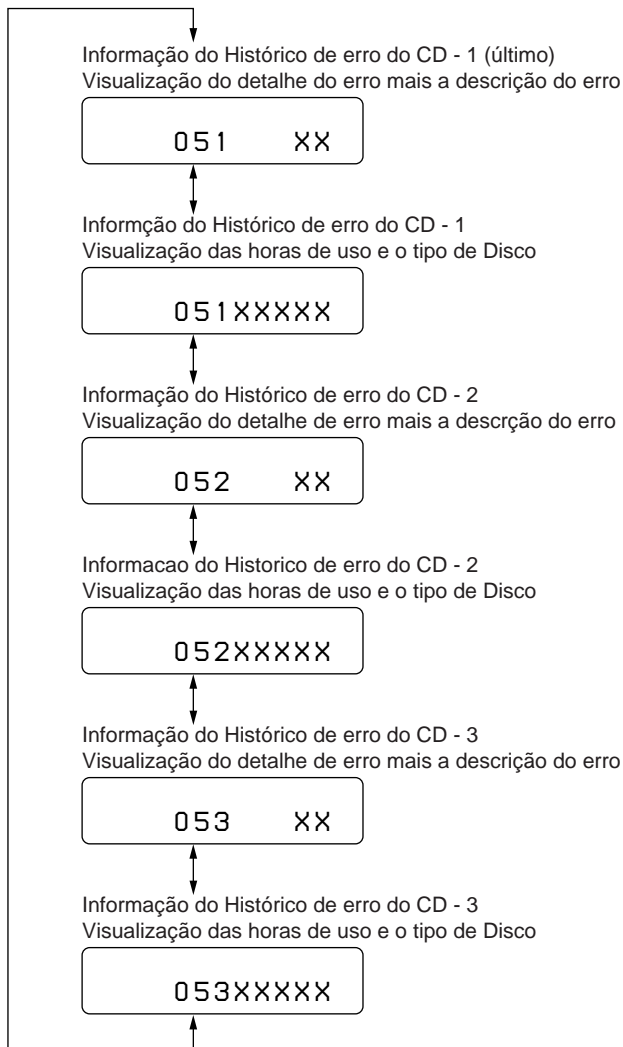
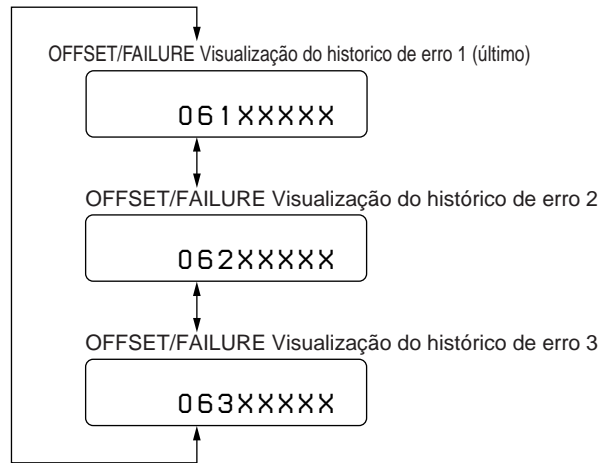


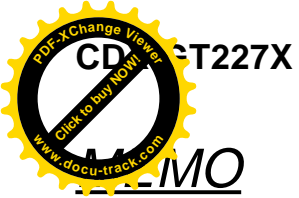
Horas de uso  
 Tipo de Disco  
 Informação Atual 1-3: 1 representa o último  
 Código de Diagnóstico 05: Informação do erro do CD

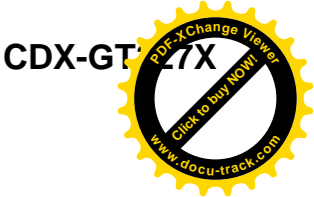
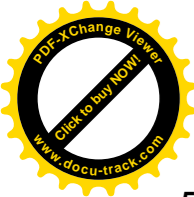
Indicação	Tipo de Disco
0	MP3
1	WMA
2	AAC
3	ATRAC
8	CD/DA
F	UNKNOWN

Esse modo é visualizado cada vez que pressionar as teclas **[2/ALBM +]** **[1/ALBM -]** durante o modo da visualização da informação do erro do CD.

Esse modo é visualizado cada vez que pressionar as teclas **[2/ALBM +]** **[1/ALBM -]** durante o modo da visualização do erro OFFSET/FAILURE.

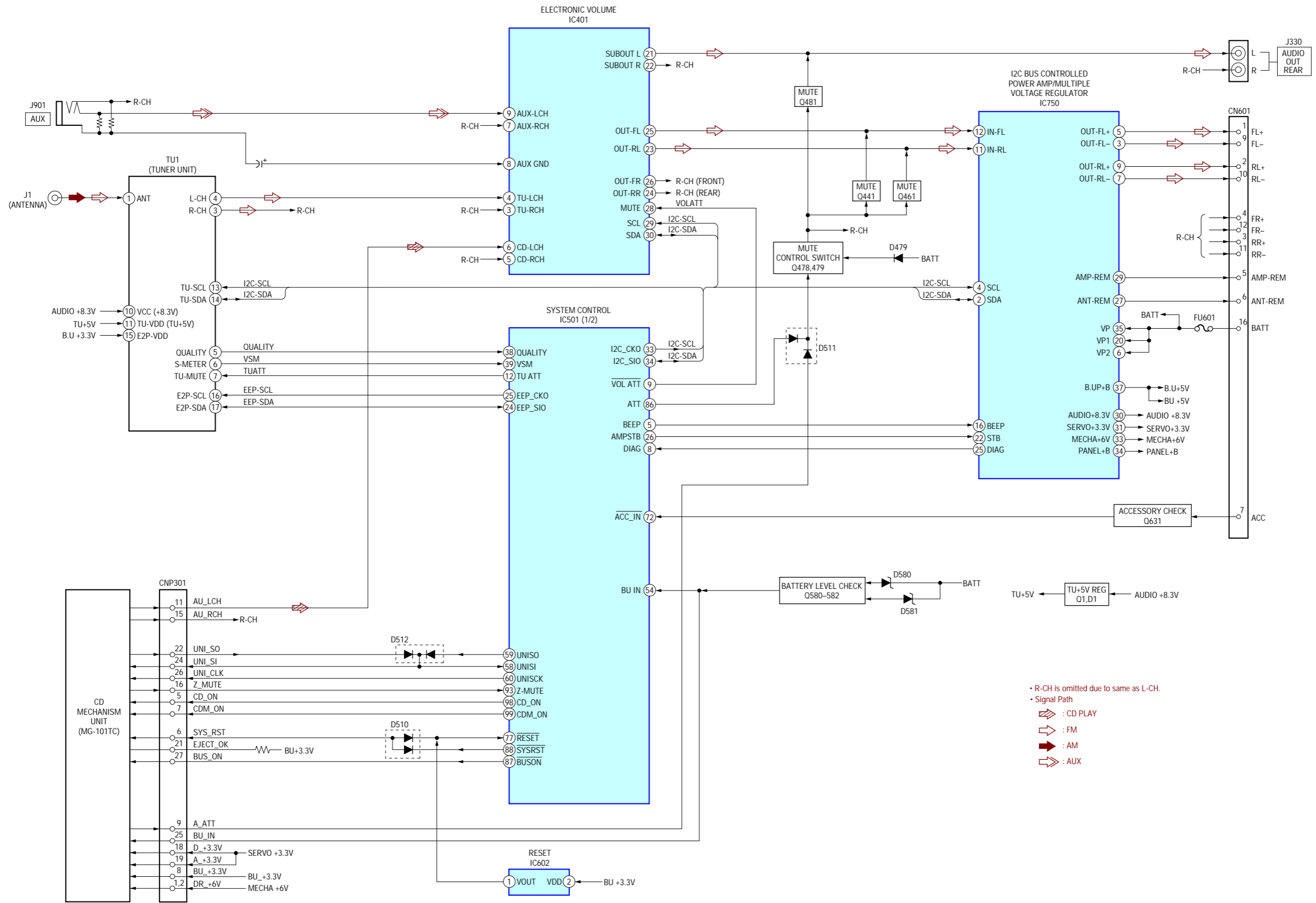




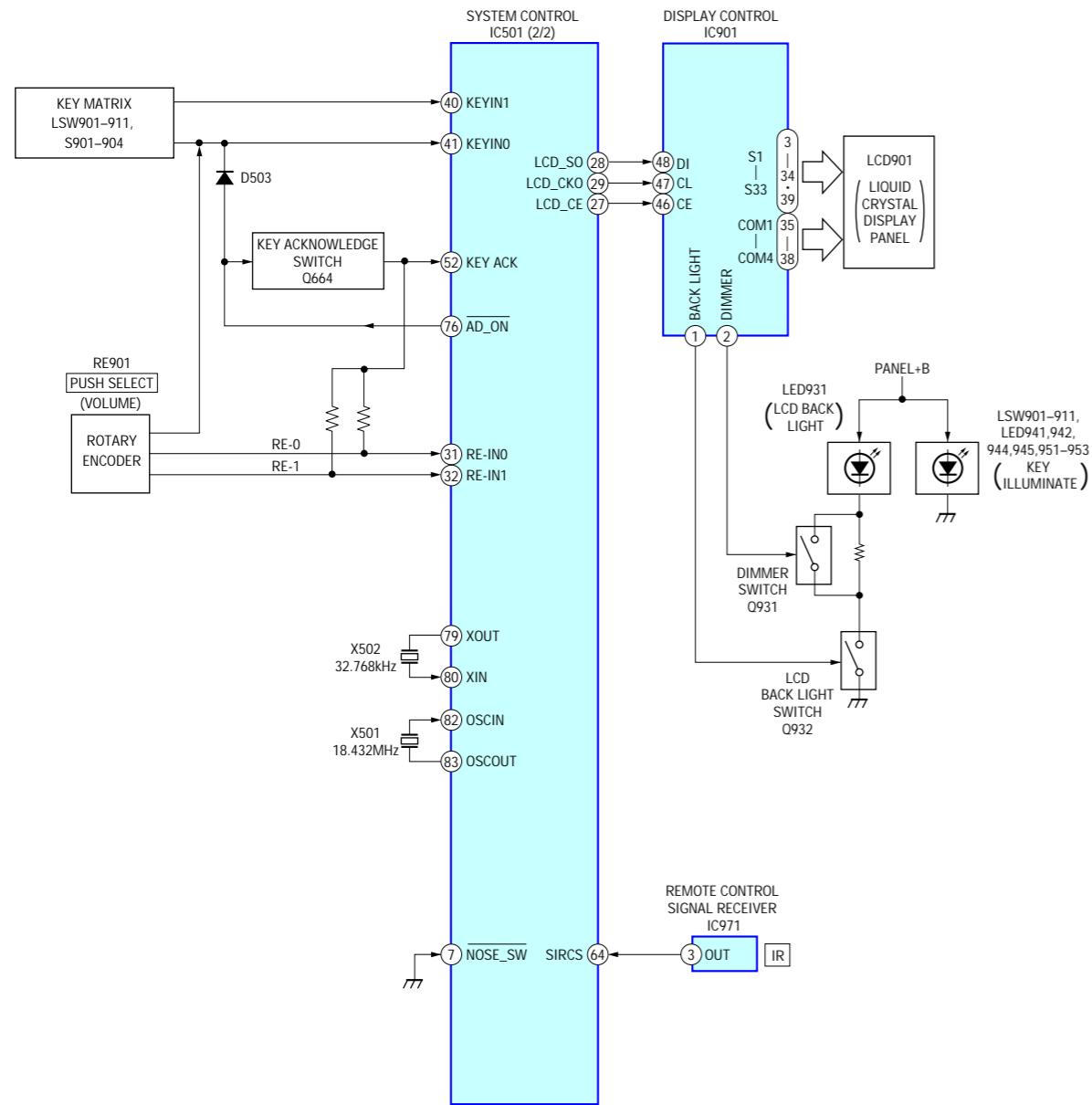


# SEÇÃO 5 DIAGRAMAS

## 5-1. DIAGRAMA EM BLOCOS — SEÇÃO PRINCIPAL —



5-2. DIAGRAMA EM BLOCOS — SEÇÃO DISPLAY —



• NOTA PARA PLACAS DE CIRCUITO IMPRESSO E DIAGRAMAS ESQUEMÁTICOS

ESTA NOTA É COMUM PARA PLACAS DE CIRCUITO IMPRESSO E DIAGRAMAS ESQUEMÁTICOS. (Em adição a isto, a nota necessária é impressa em cada boco.)

Para diagramas esquemáticos.

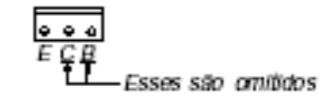
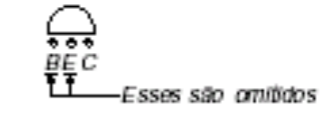
- Nota:
- Todos os capacitores estão em  $\mu\text{F}$  ou descrito diferente. (p: pF) 50  $\mu\text{W}$  ou menos não são indicados exceto para eletrolíticos e tântalos.
  - Todos os resistores estão em  $\Omega$  e  $\frac{1}{4}\text{W}$  ou a menos de outra forma especificada.
  - $\Delta$  : componente interno
  - $\square$  : designação do painel.

Para placas de circuito impresso.

- Nota:
- $\circ$  : Peças tiradas do lado dos componentes.
  - $\text{---}$  : Peças tiradas do lado condutor.
  - $\circ$  : Furo vasado.
  - $\square$  : Lado da trilha que não é vista. (As demais trilhas não são indicadas.)

Nota:  
Os componentes identificados pela marca  $\Delta$  ou linha pontilhada  $\Delta$  são críticos para segurança. Substitua somente por especificada.

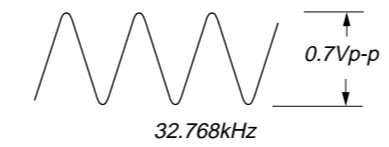
- $\text{---}$  : linha +B.
- $\text{---}$  : linha -B.
- $\square$  : ajuste para reparo.
- Voltagens e formas de ondas são de com relação a terra sob condições de sem sinal.
- Seção mecanismo do CD sem indicação : CD PLAY
- Principal (1/3), (2/3), (3/3) e seção Key (painel) sem indicação : FM
- ( ) : AM
- < > : CD PLAY
- \* : Impossível de medição
- As tensões são medidas com VOM (impedância de 10 M $\Omega$ ). As variações de tensões podem ser notadas devido a tolerâncias normais de produção.
- As formas de ondas são medidas com um osciloscópio.



- Números com um círculo referem-se a formas de onda
- Simbologia
- $\square$  : CD PLAY
- $\square$  : FM
- $\square$  : AM
- $\square$  : AUX

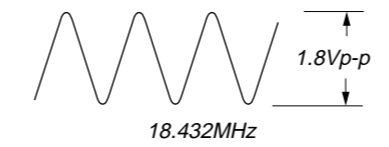
— Placa PRINCIPAL —

① IC501 ⑦9 (XOUT)



0.5 V/DIV, 20  $\mu\text{sec}/\text{DIV}$

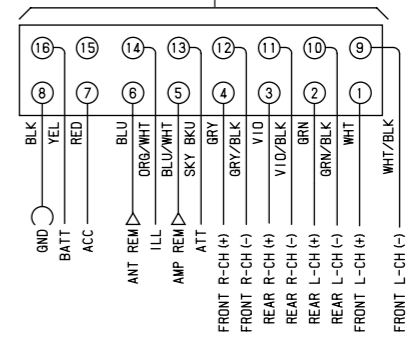
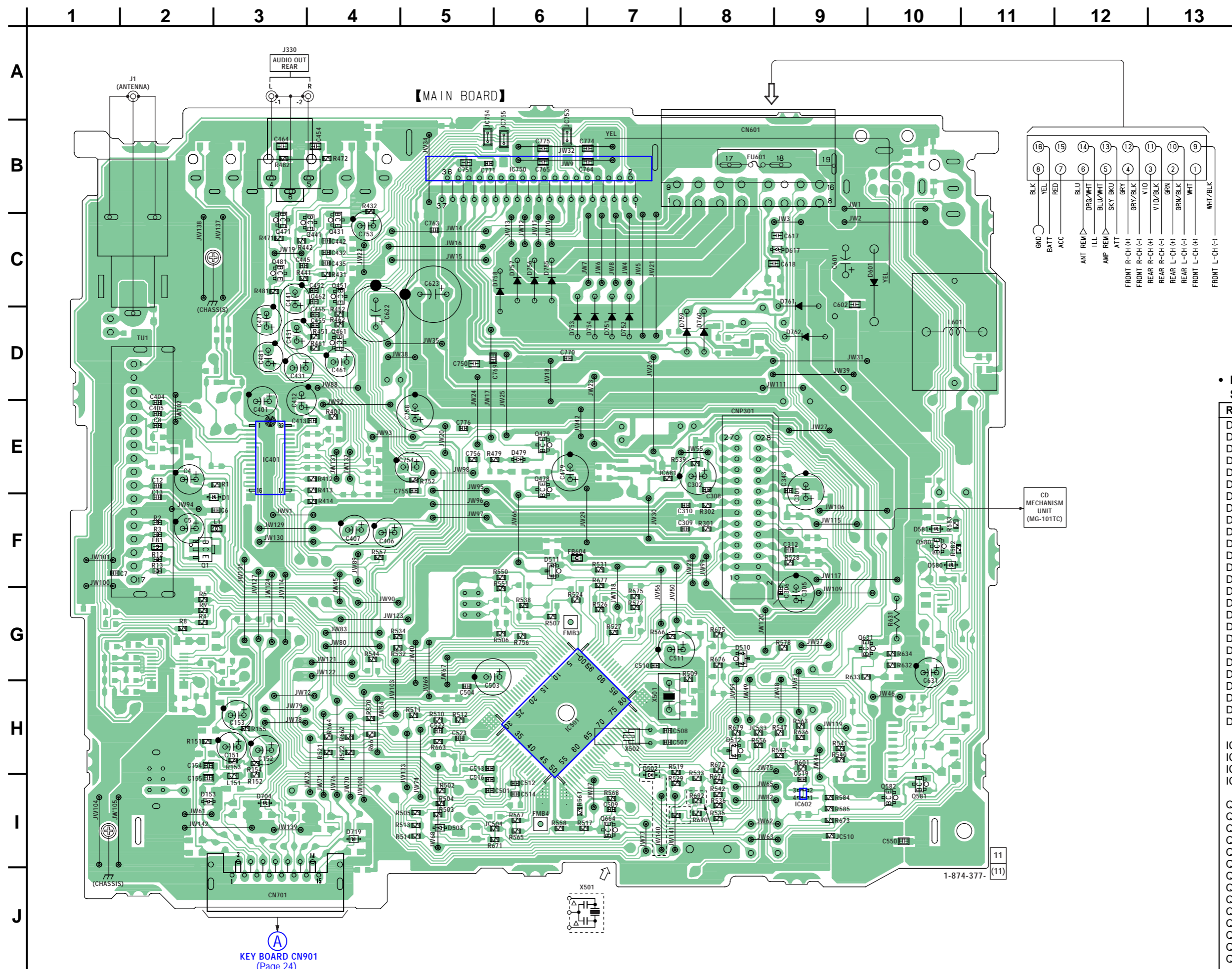
② IC501 ⑧3 (OSCOU)



0.5 V/DIV, 20 nsec/DIV

5-3. PLACA DE CIRCUITO IMPRESSO — SEÇÃO PRINCIPAL —

Lf : Utilizada solda sem chumbo.



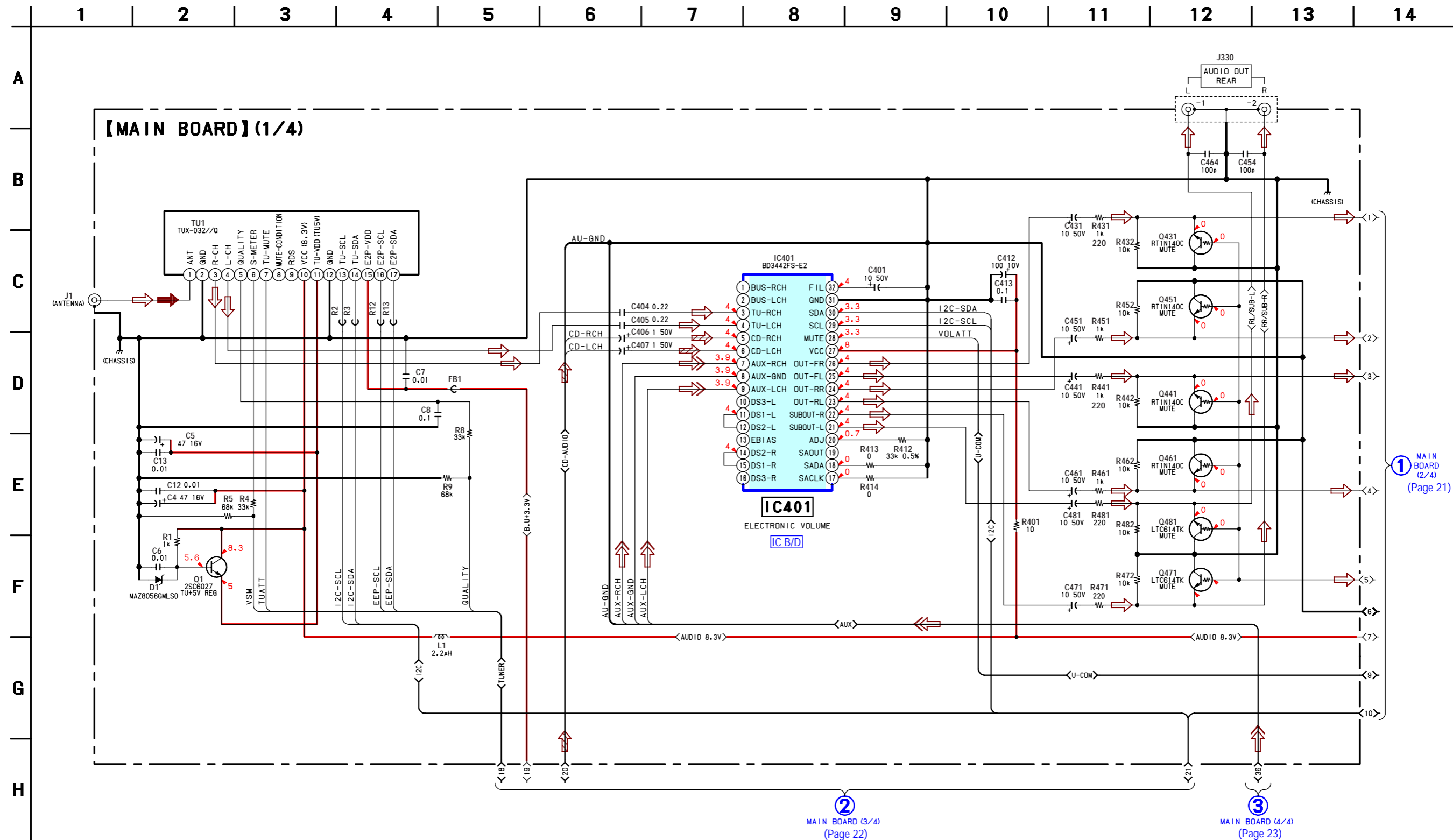
• Localização do Semicondutor

Ref. No.	Location
D1	F-3
D153	I-2
D479	E-6
D502	H-7
D503	I-5
D510	G-8
D511	F-6
D512	H-8
D580	F-10
D581	F-10
D601	C-10
D617	C-9
D704	I-3
D719	I-4
D751	D-7
D752	D-7
D753	D-6
D754	D-7
D755	C-6
D756	C-6
D757	C-6
D758	C-6
D760	D-8
D761	C-9
D762	D-9
IC401	E-3
IC501	H-6
IC602	I-9
IC750	B-6
Q1	F-2
Q431	C-4
Q441	C-4
Q451	C-4
Q461	D-4
Q471	C-3
Q478	E-6
Q479	E-6
Q481	C-3
Q580	F-10
Q581	I-10
Q631	G-9
Q664	I-7

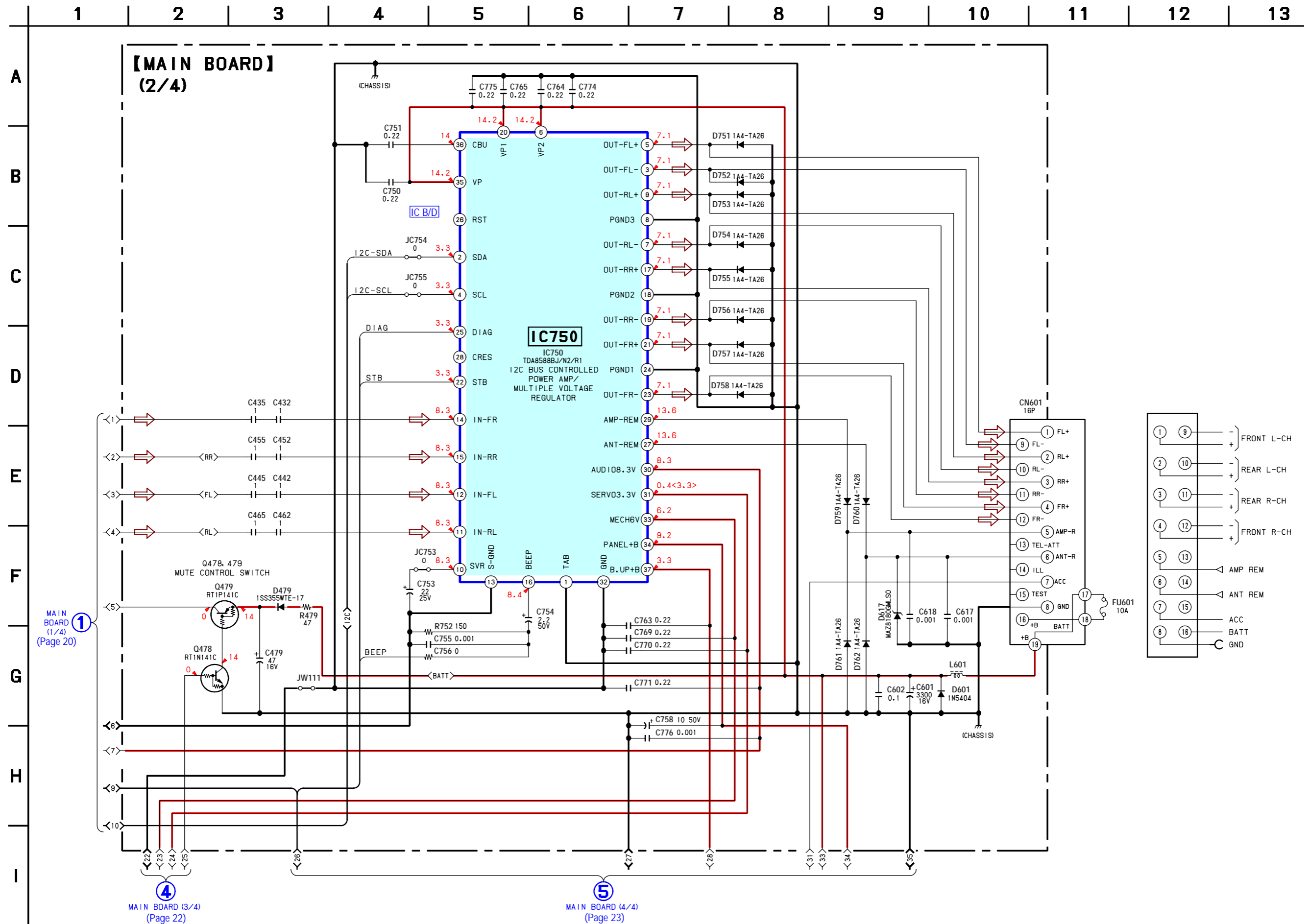
KEY BOARD CN901  
(Page 24)

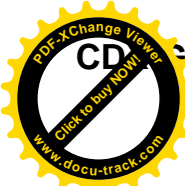


5-4. DIAGRAMA ESQUEMÁTICO — SEÇÃO PRINCIPAL (1/4) —

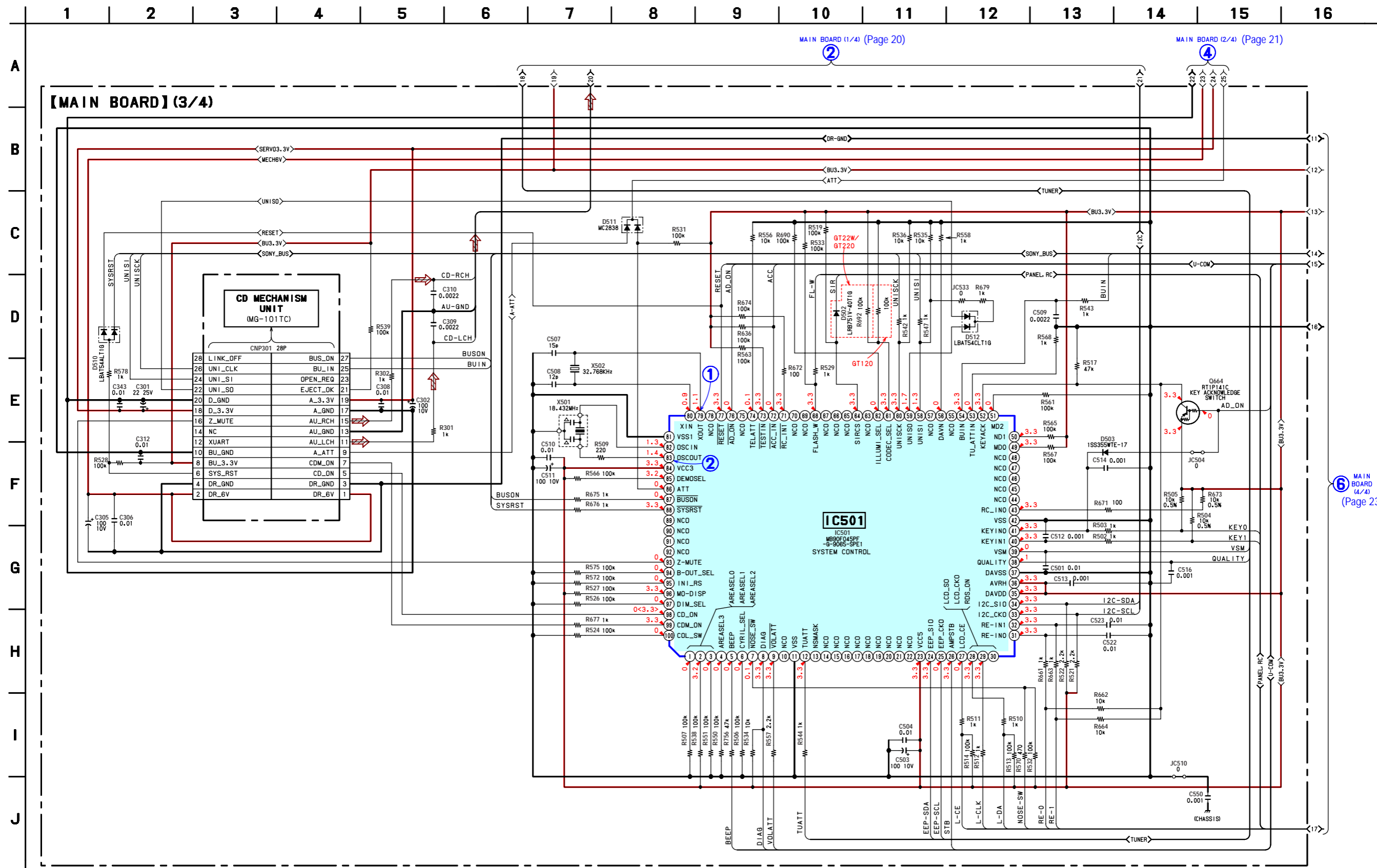


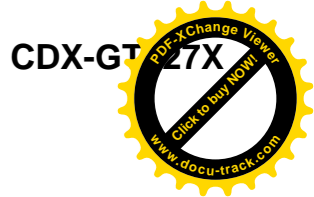
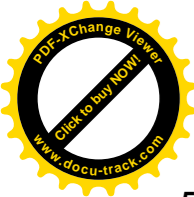
5-5. DIAGRAMA ESQUEMÁTICO — SEÇÃO PRINCIPAL (2/4) —



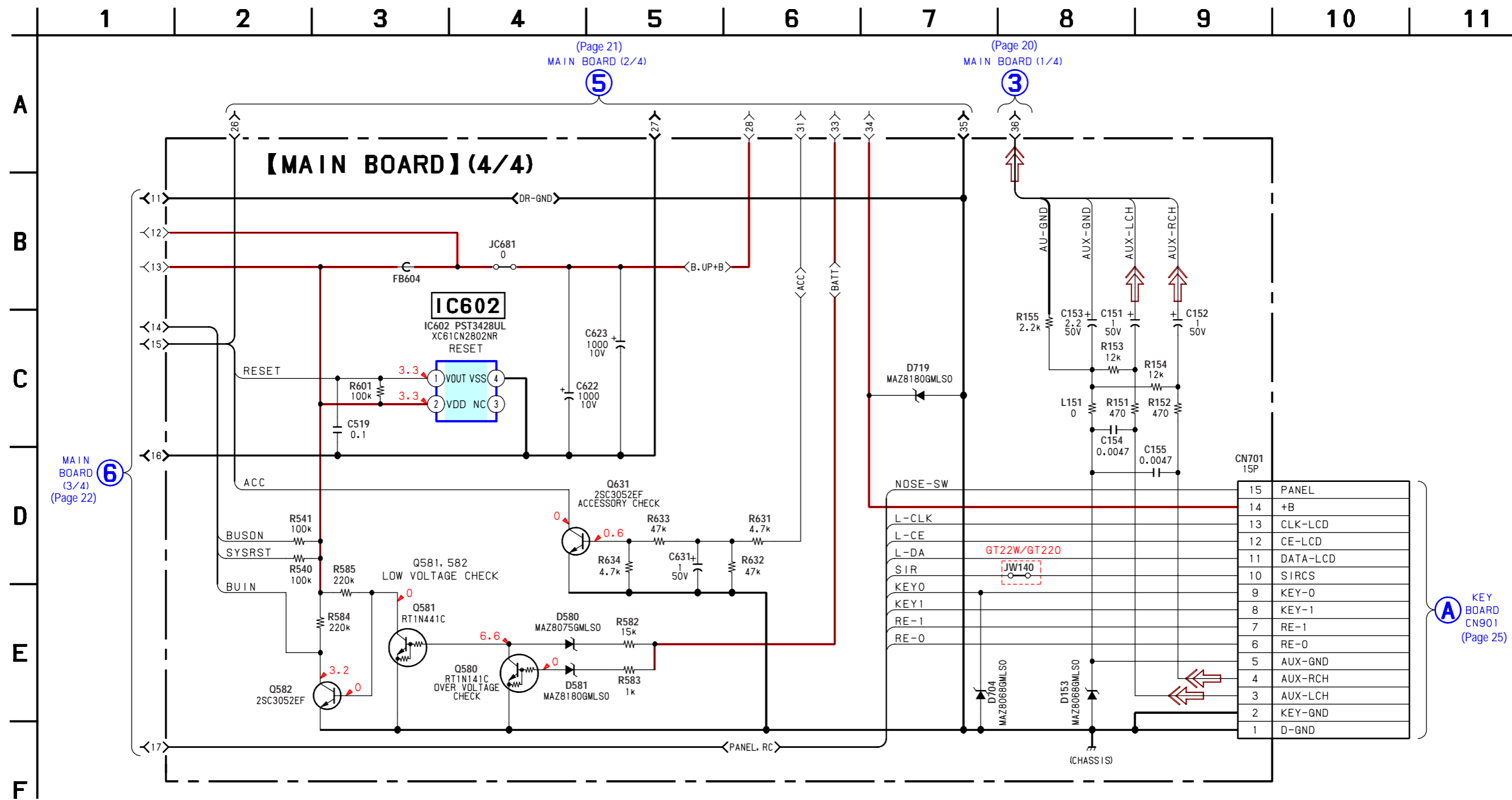


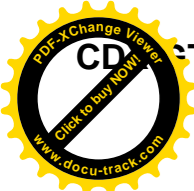
5-6. DIAGRAMA ESQUEMÁTICO — SEÇÃO PRINCIPAL (3/4) —





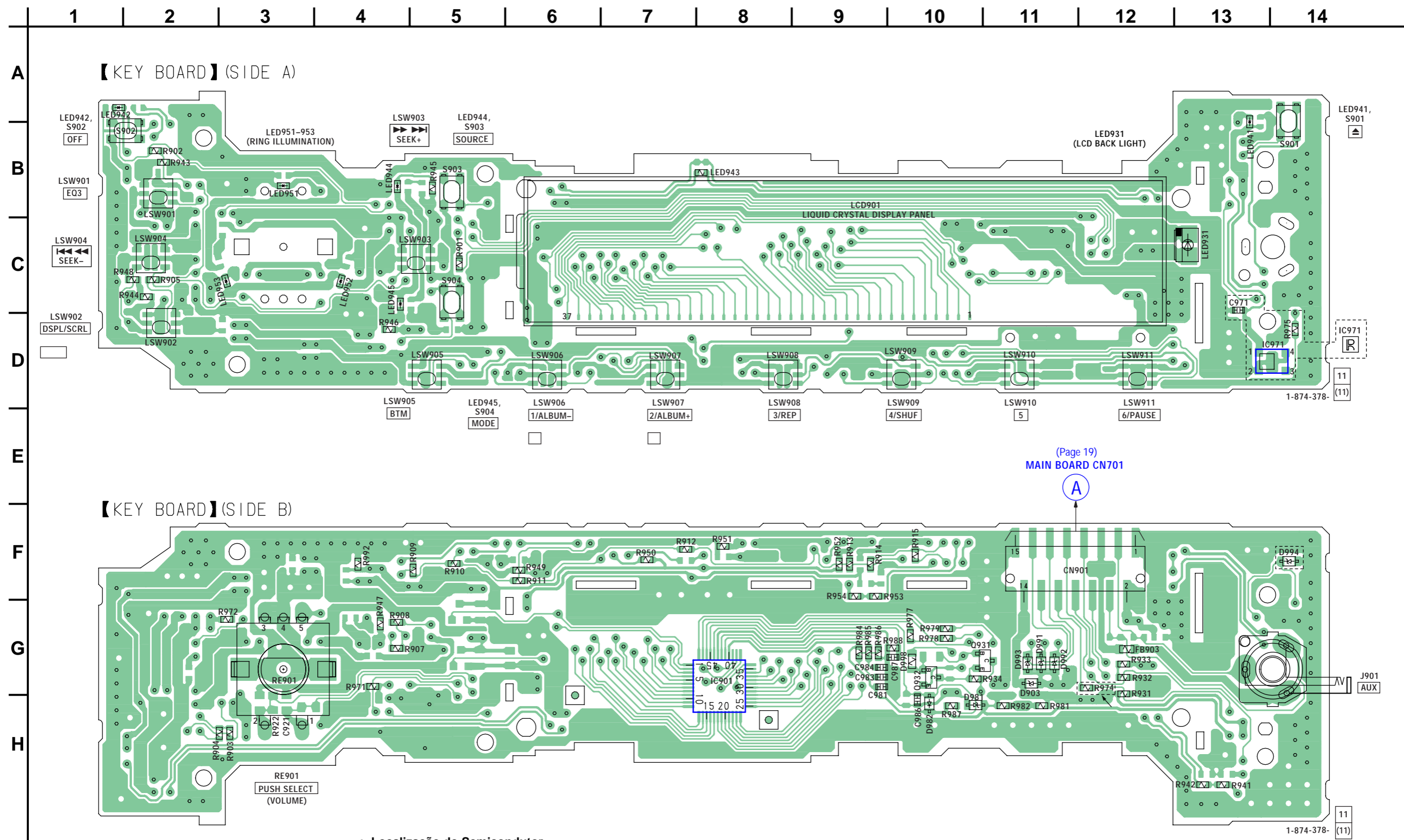
5-7. DIAGRAMA ESQUEMÁTICO — SEÇÃO PRINCIPAL (4/4) —





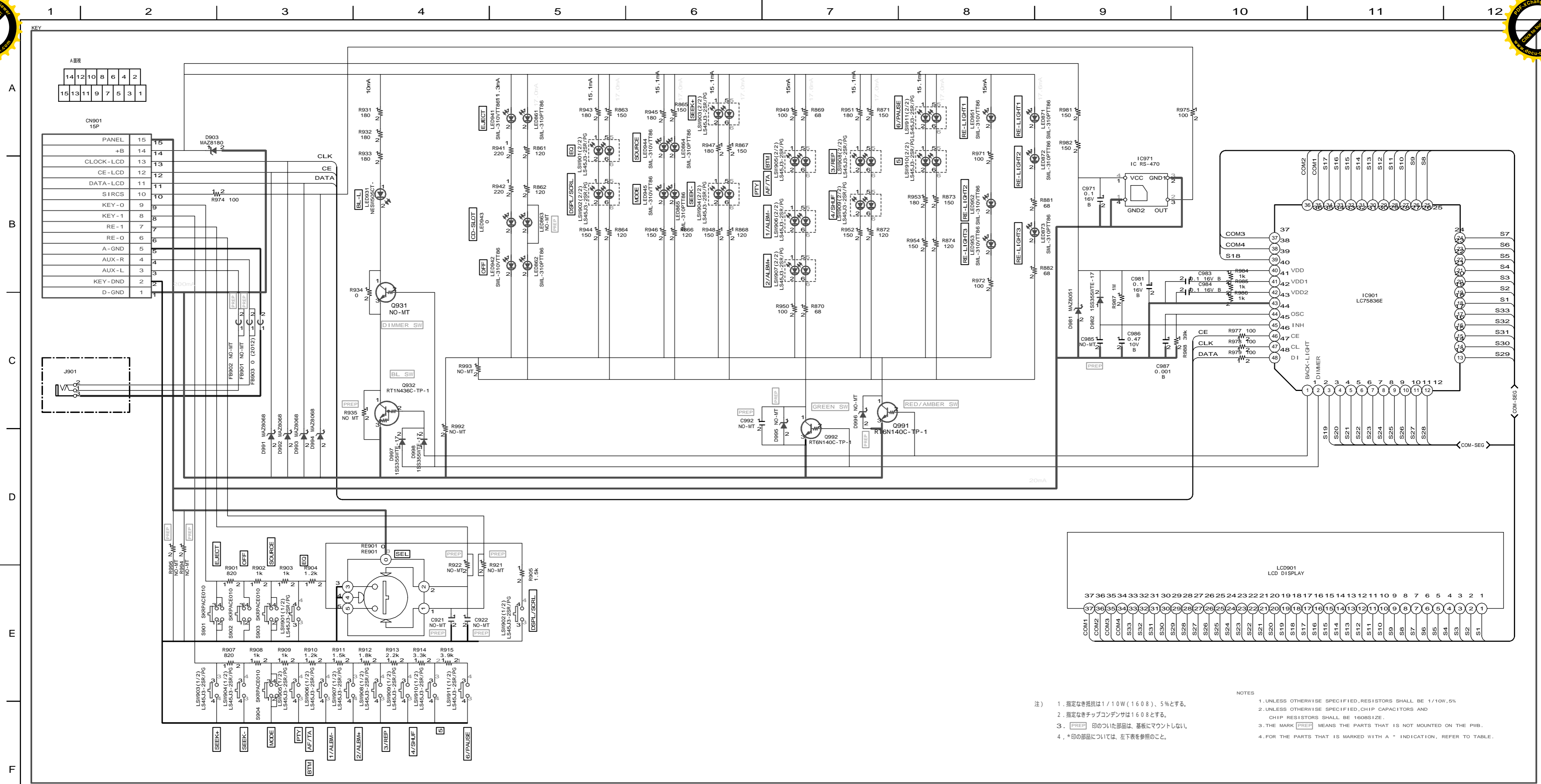
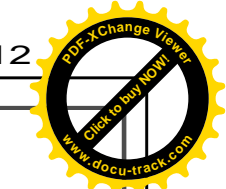
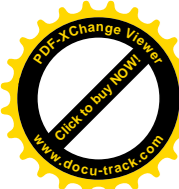
5-8. PLACA DE CIRCUITO IMPRESSO — SEÇÃO KEY —

LF : Utilizada solda sem chumbo.



• Localização do Semicondutor

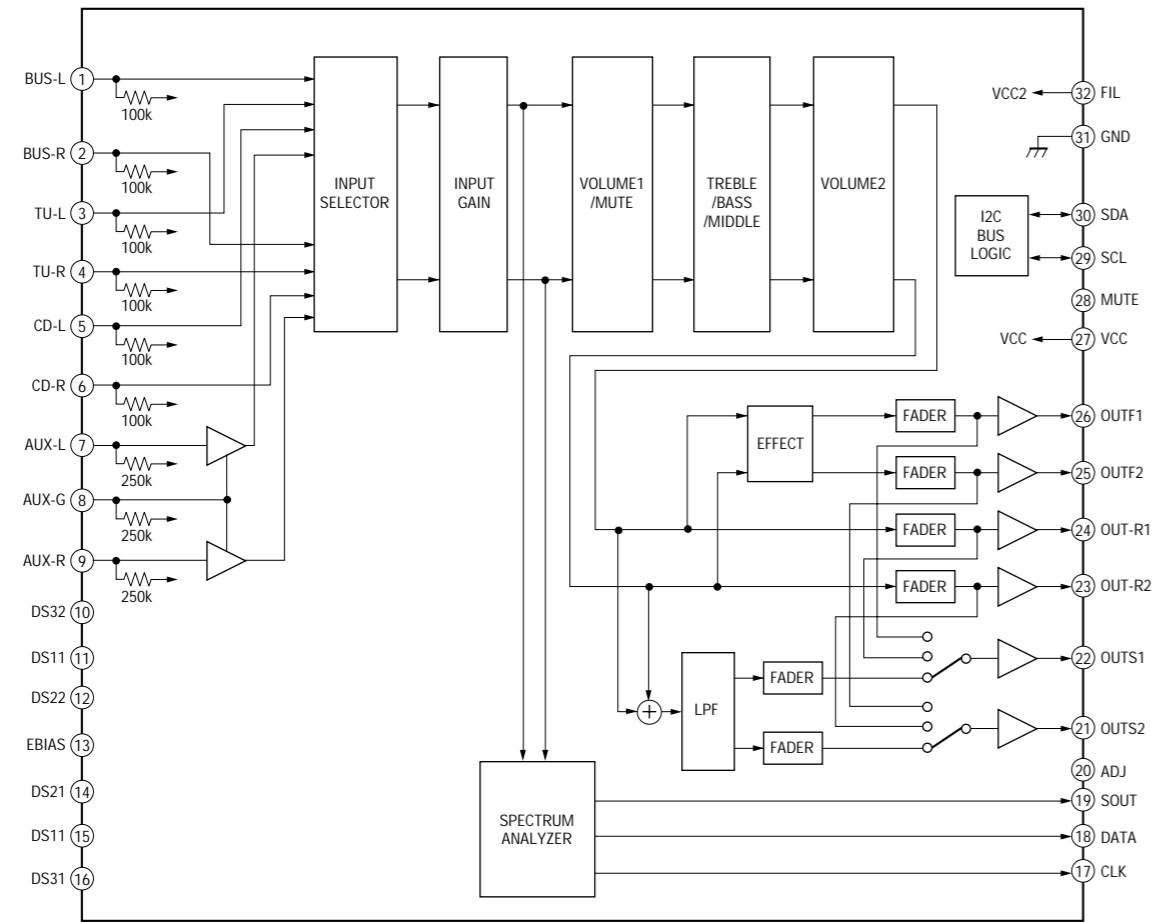
Ref. No.	Location	Ref. No.	Location	Ref. No.	Location
D903	G-11	IC901	G-8	LED945	C-4
D981	H-10	IC971	D-14	LED951	B-3
D982	H-10			LED952	C-4
D991	G-11	LED931	C-13	LED953	C-3
D992	G-11	LED941	B-13		
D993	G-11	LED942	A-1	Q931	G-10
D994	F-14	LED944	B-4	Q932	G-10



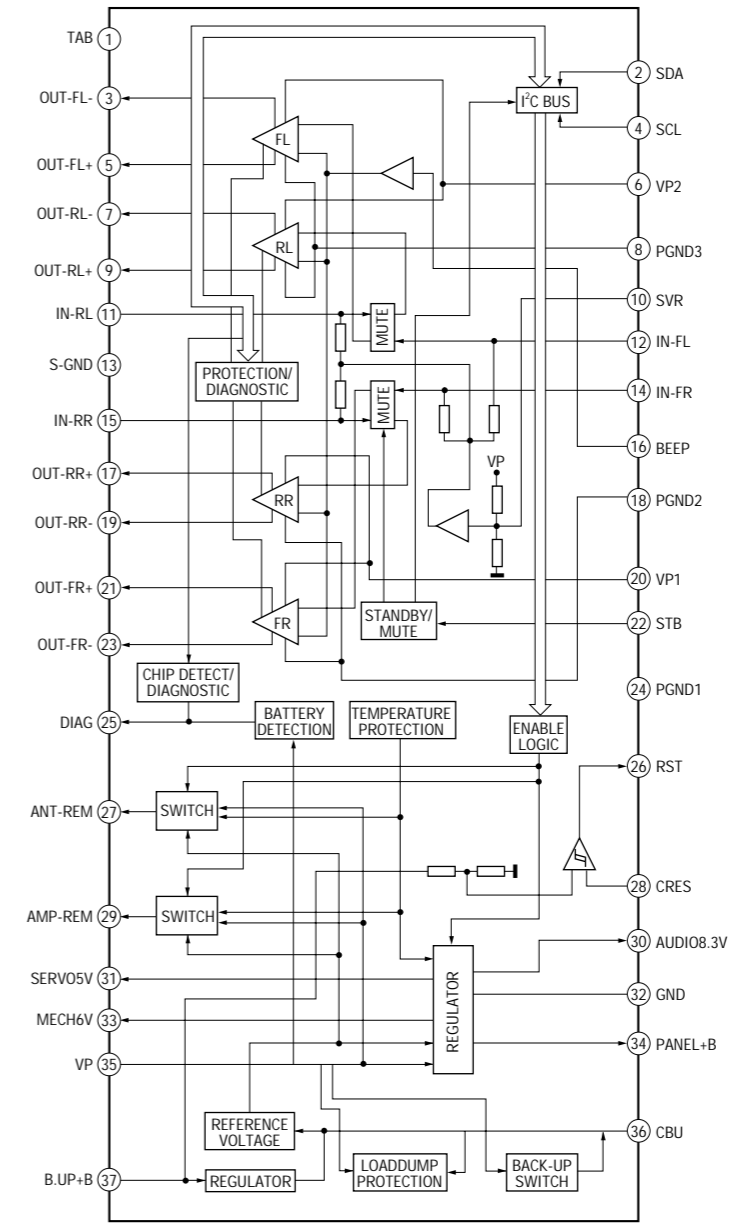
- 注)
1. 指定なき抵抗は1/10W(1608)、5%とする。
  2. 指定なきチップコンデンサは1608とする。
  3. [PREP]印のついた部品は、基板にマウントしない。
  4. \*印の部品については、左下表を参照のこと。

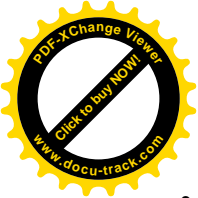
- NOTES
1. UNLESS OTHERWISE SPECIFIED, RESISTORS SHALL BE 1/10W, 5%.
  2. UNLESS OTHERWISE SPECIFIED, CHIP CAPACITORS AND CHIP RESISTORS SHALL BE 1608SIZE.
  3. THE MARK [PREP] MEANS THE PARTS THAT IS NOT MOUNTED ON THE PWB.
  4. FOR THE PARTS THAT IS MARKED WITH A \* INDICATION, REFER TO TABLE.

IC401 BD3442FS-E2 (Placa PRINCIPAL (1/4))



IC750 TDA8588BJ/N2/R1 Placa PRINCIPAL (2/4))



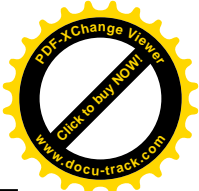
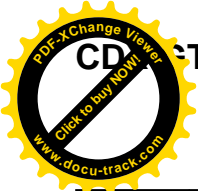


• DESCRIÇÕES DE PINOS DO IC

• IC501 MB90F045PF-G-9065-SPE1 (SYSTEM CONTROL) (PLACA PRINCIPAL (3/4))

Pin No.	Pin Name	I/O	Pin Description
1	AREASEL0	I	Destination function setting pin 0
2	AREASEL1	I	Destination function setting pin 1
3	AREASEL2	I	Destination function setting pin 2
4	AREASEL3	I	Destination function setting pin 3
5	BEEP	O	BEEP signal output to power amplifier
6	CYRIL_SEL	I	Cyril correspondence discrimination signal input "L": No correspondence
7	NOSE_SW	I	Front panel attachment detect signal input "L": Panel on, "H": Panel off
8	DIAG	I	Status signal input from power amplifier
9	VOLATT	O	Electronic volume attenuate control signal output
10	NCO	O	Not used. (Open)
11	VSS	—	Ground pin
12	TUATT	O	Tuner mute control signal output
13	NSMASK	O	Noise mask signal output Not used in this set. (Open)
14 to 22	NCO	O	Not used. (Open)
23	VCC5	—	Power supply pin (+3.3V)
24	EEP_SIO	I/O	EEPROM bus serial data signal input/output
25	EEP_CKO	O	EEPROM bus serial clock signal output
26	AMPSTB	O	Stand-by signal output to power regulation
27	LCD_CE	O	Chip enable signal output to LCD driver
28	LCD_SO	O	Serial data signal output to LCD driver
29	LCD_CKO	O	Serial clock signal output to LCD driver
30	RDS ON	O	RDS (radio data system) ON signal output Not used in this set. (Open)
31	RE-IN0	I	Rotary encoder signal input 0
32	RE-IN1	I	Rotary encoder signal input 1
33	I2C_CKO	O	I2C bus serial clock signal output
34	I2C_SIO	I/O	I2C bus serial data signal input/output
35	DAVDD	—	A/D converter power supply pin (+3.3V)
36	AVRH	—	A/D converter external reference power supply pin (+3.3V)
37	DAVSS	—	A/D converter Ground pin
38	QUALITY	I	Noise detect signal input
39	VSM	I	S-meter voltage detect signal input
40	KEYIN1	I	Key signal input 1
41	KEYIN0	I	Key signal input 0
42	VSS	—	Ground pin
43	RC_IN0	I	Rotary commander key signal input Not used in this set. (Pull up)
44 to 48	NCO	O	Not used. (Open)
49	MD0	I	Operation mode setting pin 0 (Pull up)
50	MD1	I	Operation mode setting pin 1 (Pull up)
51	MD2	I	Operation mode setting pin 2 (Pull down)
52	KEYACK	I	Key acknowledgment detect signal input
53	TU_ATTIN	I	Tuner mute zero cross detect signal input Not used in this set. (Pull down)
54	BUIN	I	Back-up power supply detect signal input
55	NCO	O	Not used. (Open)
56	DAVN	I	RDS data block synchronized detect signal input Not used in this set. (Pull down)
57	NCO	O	Not used. (Open)
58	UNISI	I	S-BUS data signal input
59	UNISO	O	S-BUS data signal output
60	UNISCK	O	S-BUS clock signal output





Pin No.	Pin Name	I/O	Pin Description
61	CODEC_SEL	I	MP3 select signal input "H": MP3, "L": Non-MP3
62	ILLUMI_SEL	I	Illumination voltage setting signal input "H": 10.4 V, "L": 9.0 V
63	NCO	O	Not used. (Open)
64	SIRCS	I	Remote control signal input
65 to 67	NCO	O	Not used. (Open)
68	FLASH_W	I	Memory mode select signal input Not used in this set. (Pull up) Normally "H" input: Single chip mode, after reset "L": Flash write mode
69, 70	NCO	O	Not used. (Open)
71	RC_IN1	I	Rotary commander shift key signal input Not used in this set. (Pull up)
72	ACC_IN	I	Accessory power supply detect signal input
73	TESTIN	I	Test mode detect signal input Not used in this set. (Pull up)
74	TELATT	I	Telephone attenuate detect signal input Not used in this set. (Pull down)
75	NCO	O	Not used. (Open)
76	AD_ON	O	A/D converter power supply control signal output
77	RESET	I	System reset signal input
78	NCO	O	Not used. (Open)
79	XOUT	O	Low speed operation clock signal output (32.768 kHz)
80	XIN	I	Low speed operation clock signal input (32.768 kHz)
81	VSS1	—	Ground pin
82	OSCIN	I	High speed operation clock signal input (18.432 MHz)
83	OSCOU	O	High speed operation clock signal output (18.432 MHz)
84	VCC3	—	Power supply pin (+3.3 V)
85	DEMOSEL	I	DEMO select signal input "H": DEMO on, "L": DEMO off
86	ATT	O	Audio mute control signal output
87	BUSON	O	BUS ON signal output
88	SYRST	O	System reset signal output
89 to 92	NCO	O	Not used. (Open)
93	Z-MUTE	I	CD zero cross mute detect signal input
94	B OUT SEL	I	Black out with/without discrimination select signal input "H": Black out
95	INI RS	I	REAR/SUB INITIAL setting signal input "H": REAR INITIAL, "L": SUB INITIAL
96	MO-DISP	I	Motion display signal input "H": Motion display on, "L": Motion display off
97	DIM_SEL	I	Dimmer select signal input "H": Dimmer, "L": No dimmer Not used in this set. (Pull down)
98	CD_ON	I	CD mechanism servo power supply control request signal input
99	CDM_ON	I	CD mechanism deck power supply control request signal input
100	COL_SW	I	Illumination color select signal input Not used in this set. (Pull down) "H": Two colors selection/initial slave, amber, "L": Color/slave, amber